ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109

(Autonomous)

BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the Twenty-sixth Meeting

Minutes of the 26th Meeting of the Board of Studies in Electronics & Communication Engineering 22.07.2022 held on at 11.00 A.M.

MEMBERS PRESENT

- 1. Dr. S. Sumathi, Chairman
- 2. Dr. T Menakadevi Internal Member
- 3. Dr.R.Poovendran, Internal Member
- 4. Mr. M. AshokKumar, Internal Member
- 5. Dr.T.S.UdhayaSuriya, Interdisciplinary Member, HoD/BME
- 6. Dr.B.Perumal,InterdisciplinaryMember,Dept of EEE
- 7. Dr.K.Senthil, Interdisciplinary Member, Dept of EEE
- 8. Prof.Dr.A.Sivanantha Raja, University Nominee
- 9. Prof.Dr.K.Mariammal, Academic Council Nominee
- 10. Prof.Dr.M.Dhinakaran, Academic Council Nominee.
- 11. Smt.SudhaM,Industry Representative
- 12. Mr. S. Munirajsankar, Alumnus Member
- 26.1 To consider the minutes of the 25th Board of Studies meeting in Electronics and Communication Engineering held on 18.09.2021.
 - **RESOLVED** that the minutes of the 25th Board of Studies meeting in Electronics and Communication Engineering held on 18.09.2021 have been considered.
- 26.2 To consider the Syllabi of 7th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019–2020.
 - **RESOLVED** that the Syllabi of 7th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019–2020 be approved (Annexure I)
- 26.3 To consider the EEC category elective course entitled "Professional Readiness for Innovation, Employability and Entrepreneurship" approved from the Centre for Academic Courses, Chennai for 7th semester B.E Electronics and Communication Engineering programme under the regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.
 - **RESOLVED** thatthe EEC category elective course entitled "Professional Readiness for Innovation, Employability and Entrepreneurship" approved from the Centre for Academic Courses, Chennai for 7th semester B.E. Electronics and Communication Engineering

programme under the regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure-II).

26.4 To consider the Syllabi of 5th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

RESOLVED that the Syllabi of 5th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure III)

26.5 To consider the Syllabi of "Microprocessors and Microcontrollers" and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

RESOLVED that the Syllabi of "Microprocessors and Microcontrollers" and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021be approved (Annexure IV)

26.6 To consider the Syllabi of 3rdSemester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2021 – 2022.

RESOLVED the Syllabi of 3rdSemester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2021 – 2022be approved. (Annexure V)

To consider the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3rd Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2021 – 2022.

RESOLVEDthat the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3rd Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2021 – 2022 be approved (Annexure VI)

26.8 To consider the Syllabus of "Communication Engineering" for 3rd Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2021 – 2022.

RESOLVED the Syllabus of "Communication Engineering" for 3^{rd} Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2021-2022 be approved (Annexure VII)

- 26.9 To consider the curriculum of B.E Electronics and Communication Engineering under the Regulations 2022 to be followed as per choice based credit systems for the batch of the students to be admitted during 2022-2023. (Under Processing)
- 26.10 To consider the Syllabi of 3rdSemester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2021-2022.
 - **RESOLVED** the Syllabi of 3rdSemester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2021-2022 be approved.(Annexure IX)
- 26.11 To consider the curriculum of M.E Communication Systems under the Regulations 2022 to be followed as per choice-based credit systems for the batch of the students to be admitted during 2022-2023.
 - **RESOLVED** consider the curriculum of M.E Communication Systems under the Regulations 2022 to be followed as per choice based credit systems for the batch of the students to be admitted during 2022-2023 be approved.(Annexure X)
- **26.12** To consider the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2022 (CBCS) for the batch of students to be admitted in 2022-2023.
 - **RESOLVED** the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2022 (CBCS) for the batch of students to be admitted in 2022-2023 be approved.(Annexure XI)
- 26.14 To consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2022-2023.

RESOLVED consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2022-2023be approved.(Annexure XIII)

Date: 22.07.2022

Signature of Chairman -BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & PC)
Adhiyamaan College of Engineering (Autonomous)
Hosur - 635 109
Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Members Present for the Twenty Sixth Board of Studies Meeting held on 22.07.2022

Sl.No.	Name & Address of the Members	Category	Signature
1	Dr. S. Sumathi, Ph.D (VLSI)., Prof & Head, Dept. of ECE, ACE, Hosur.	BoS-Chairman	Julean
2	Dr. T.Menakadevi., M. Tech (VLSI), Ph.D., Professor, Dept. of ECE, ACE, Hosur.	Internal Member	1 Oleber
3	Dr.R.Poovendran.,P.h.D., AP, Dept. of ECE, ACE, Hosur.	Internal Member	Mr
4	Mr. M. Ashok Kumar, M.E, Assistant professor, Dept. of ECE, ACE, Hosur.	Internal Member	Y Enh
5	Dr.T.S.UdhayaSuriya Professor & Head Department of BME, ACE, Hosur.	Interdisciplinary Member	Long
6	Prof.Dr.B.Perumal, Ph.D.,Asso.Professor& Head/Dept. of EEE Adhiyamaan College of Engineering, Hosur.	Interdisciplinary Member	Rec
7	Prof.Dr.K.Senthil, Ph.D., Asso. Professor/Dept. of EEE Adhiyamaan College of Engineering, Hosur.	Interdisciplinary Member	land
8	Prof.Dr.ASivanantha Raja.,Ph.D Professor & Head ,Dept. of ECE, AlagappaChettiar Government College of Engineering and Technology,Karaikudi	University Nominee	Online mode
9	Prof.Dr. K.Mariammal.,Ph.D., Assistant Professor (Sr Gr), Department of Electronics Engineering, Madras Institute of Technology Anna University, Chennai.	Academic Council Nominee	Online mode
10	Prof.Dr. M.Dhinakaran., Ph.D., Associate Professor, Dept. of ECE, Government College of Engineering, Salem	Academic Council Nominee	Online mode
11	Smt. Sudha M Larsen and Toubro (L & T) Defence, Bangalore,	Industry Representative	Online mode
12	Mr. S. MunirajSankar Senior Specialist, HCL Technologies Ltd, Chennai	Alumnus Member	Online mode

Date: 22.07.2022

Signature of the Chairman-BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & CC)
Adhiyamaan College of Engineering (Autonomous)
Hosur. - 535 109
Krishnagiri (Dt), Tamii Nadu.

	:	Department of ECE- 26 th Bos	Recommendations
Sem	Course Code	Course Name	Remarks (New course Introduced/ Contents incorporated in the corresponding course)
	:	2022-202 Under UG Programme Regulat	•
VII	718ECP09	Professional Readiness for Innovation, Employability and Entrepreneurship	As per the Centre for Academic Courses, Anna University, Chennai, this course is introduced for 7th semester B.E Electronics and Communication Engineering programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020
		2022-202	3
		Under PG Programme Regulat	ions-2022 Curriculum
	122COT03	Modern Digital Communication Techniques	The existing syllabus has been reframed with the following unit modules UNIT II-EQUALIZATION TECHNIQUES Band Limited Channels- ISI – Nyquist Criterion-Controlled ISI-Partial Response signals, Equalization algorithms— Linear equalizer — Decision feedback equalization — Adaptive Equalization algorithms. UNIT V-MULTICARRIER AND MULTIUSER COMMUNICATIONS Single Vs multicarrier modulation, orthogonal frequency division multiplexing (OFDM), Modulation and demodulation in an OFDM system, An FFT algorithmic implementation of an OFDM system, Bit and power allocation in multicarrier modulation, Peak-to-average ratio in multicarrier modulation. Introduction to CDMA systems, multiuser detection in CDMA systems — optimum multiuser receiver, suboptimum detectors, successive interference cancellation.
I	122COT04	Advanced Wireless / Communication	Newly Introduced Course

	·		
	122COT05	Advanced Radiation Systems	The existing syllabus has been reframed with the following unit modules UNIT IV-MODERN ANTENNAS & MEASUREMENT TECHNIQUES Base station antennas, PIFA — Antennas for WBAN — RFID Antennas — Automotive antennas, MIMO Antennas, Diversity techniques — Antenna impedance and radiation pattern measurements
			UNIT V- RECENT TRENDS IN ANTENNA DESIGN UWB antenna arrays — Vivaldi antenna arrays — Artificial magnetic conductors/High impedance surfaces — Antennas in medicine — Plasma antennas — Antennas for millimeter wave communication - optimization techniques — Numerical methods
1	122COT06	Research Methodology& IPR	Newly Introduced Course
1	122COAXX	Audit Course-1* /	Newly Introduced Non-Credit Course
1	122COP02	Advanced Digital Signal Processing Laboratory	Newly Introduced
11	222COT01	RF system design	Newly Introduced Course
JI.	222COT03	Advanced Wireless Networks	Newly Introduced Course
TI .	222COOXX	Open Elective	Newly Introduced Course offered by other Departments
11	222COAXX	Audit Course-II	Newly Introduced Non-Credit Course
11	222COP02	Term Paper and Seminar	Newly Introduced
11	222COE06	Advanced Satellite Communication and Navigation systems	Newly Introduced Elective Course
11	222COE10	mm Wave Communication	Newly Introduced Elective Course
II	222COE12	Sensor Networks and IoT	Newly Introduced Elective Course
111	322COT01	Optical and Mobile Communication Networks	Newly Introduced Course
,III	322COT02	Pattern Recognition and Machine Learning	Newly Introduced Course

		Newly Introduced Elective Course
322COE06	Radio Defined	
322COE12	Remote Sensing	Newly Introduced Elective Course
X22COA01	English for Research Paper Writing	
X22COA02	Disaster Management	Newly Introduced Audit courses (Non-Credit Course)
X22COA03	Constitution of India	·
X22COA04	நற்றமிழ் இலக்கியம்	
	322COE12 X22COA01 X22COA02 X22COA03	Radio 322COE12 Remote Sensing English for Research Paper Writing X22COA02 Disaster Management X22COA03 Constitution of India

Feedback obtained about curriculum and syllabus from stakeholders (Students, Faculty, Employers & Alumni) were also discussed and concluded.

BoS Chairman

Chairman, Board of Studies

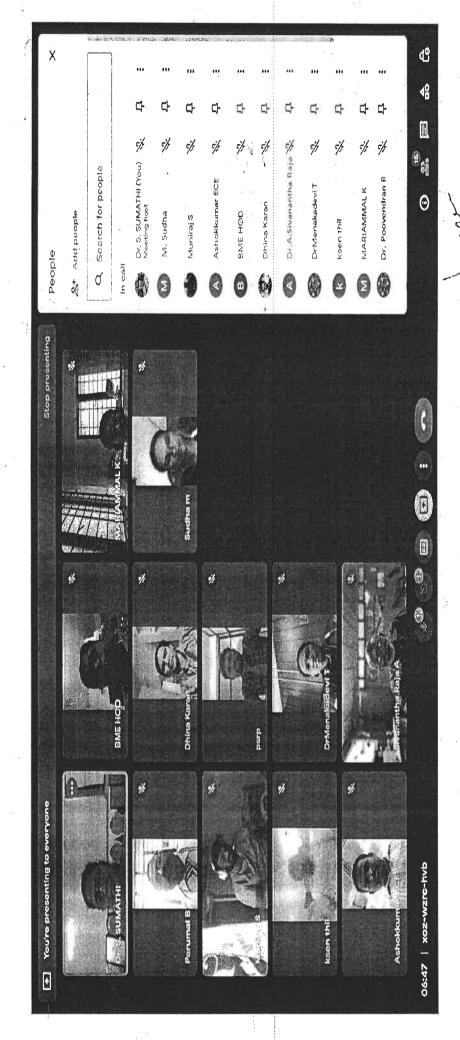
Faculty of Electronics and Communication Engineering (UG & FC)

Adhiyamaan College of Engineering (Autonomous)

Hosur - 635 109

Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING|AUTONOMOUS|, HOSUR-635109 FACULTY OF ELECTRONICS & COMMUNICATION ENGINEERING [UG&PG] PHOTO OF 26th BoS MEETING ON 22.07.2022



Signature of Chairman -BoS

Chairman, Board of Studies
Adhiyamaan College of Engineering (UG & FC) Krishnagiri (Dt), Tamil Nadu.

Date: 22.07.2022

ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109

(Autonomous)

BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the Twenty-Fifth Meeting

Minutes of the 25th Meeting of the Board of Studies in Electronics & Communication Engineering held on 18.09.2021 at 09.30 A.M.

MEMBERS PRESENT

- 1. Dr. S. Sumathi, Chairman
- 2. Dr. T Menakadevi Internal Member
- 3. Dr.R.Poovendran, Internal Member
- 4. Dr.N.Nagaraju, Internal Member
- 5. Dr.T.S.UdhayaSuriya, Interdisciplinary Member, HoD/BME
- 6. Dr.B.Perumal, Interdisciplinary Member, Dept of EEE
- 7. Dr.K.Senthil, Interdisciplinary Member, Dept of EEE
- 8. Prof.Dr.A.Sivanantha Raja, University Nominee
- 9. Prof. Dr. K. Mariammal, Academic Council Nominee
- 10. Prof.Dr.M.Dhinakaran, Academic Council Nominee
- 11. Smt.Sudha M,Industry Representative
- 12. Mr. S. Munirajsankar, Alumnus Member
- 25.1 To consider the minutes of the 24th Board of Studies meeting in Electronics and Communication Engineering held on 27.03.2021.
 - **RESOLVED** that the minutes of the 24th Board of Studies meeting in Electronics and Communication Engineering held on 27.03.2021 have been considered.
- 25.2 To consider the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018–2019.
 - **RESOLVED** that the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018–2019 be approved (Annexure I)
- 25.3 To consider the Syllabi of 6th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 2020.
 - **RESOLVED** that the Syllabi of 6th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 2020 be approved (Annexure II)

25.4 To consider the Syllabi of 4th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

RESOLVED that the Syllabi of 4th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure III)

25.5 To consider the Syllabi of "Electric Circuits and Electron Devices" and "Circuits and Devices Laboratory" for 2nd Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021 – 2022.

RESOLVED the Syllabi of "Electric Circuits and Electron Devices" and "Circuits and Devices Laboratory" for 2nd Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021 – 2022 be approved. (Annexure IV)

25.6 To consider the Syllabi of 4th Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021.

RESOLVED that the Syllabi of 4th Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021 be approved (Annexure V)

To consider the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021-2022.

RESOLVED that the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021-2022 be approved (Annexure VI)

25.8 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

RESOLVED the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved. (Annexure VII)

25.9 To consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2021-2022.

RESOLVED that the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2021-2022 be approved. (Annexure VIII)

Date: 07.10.2020

Signature of Chairman -BoS

Chairman, Board of Studies

Faculty of Electronics and Communication Engineering (Ch. F.C.)

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ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Members Present for the Twenty Fifth Board of Studies Meeting held on 18.09.2021

Sl.No.	Name & Address of the Members	Category	Signature
1	Dr. S. Sumathi, Ph.D (VLSI)., Prof & Head, Dept. of ECE, ACE, Hosur.	BoS-Chairman	Julaen
2	Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D., Professor, Dept. of ECE, ACE, Hosur.	Internal Member	TULL
3	Dr.R.Poovendran., M.E., (VLSI Design) AP, Dept. of ECE, ACE, Hosur.	Internal Member	D1 -
4	Prof.Dr. N.Nagaraju., Ph.D., Asst. Professor, Dept.of ECE, ACE, Hosur	Internal Member	N. Neposet
-5	Dr.T.S.Udhaya Suriya Professor & Head Department of BME, ACE, Hosur.	Interdisciplinary Member	Sour
6	Prof.Dr.B.Perumal, Ph.D.,Asso.Professor/Dept. of EEE Adhiyamaan College of Engineering, Hosur.	Interdisciplinary Member	RU
7	Prof.Dr.K.Senthil, Ph.D., Asso. Professor / Dept. of EEE Adhiyamaan College of Engineering, Hosur.	Interdisciplinary Member	Veson
8	Prof.Dr.A Sivanantha Raja.,Ph.D Professor & Head ,Dept. of ECE, Alagappa Chettiar Government College of Engineering and Technology,Karaikudi	University Nominee	Online mode
9	Prof.Dr. K.Mariammal., Ph.D., Assistant Professor (Sr Gr), Department of Electronics Engineering, Madras Institute of Technology Anna University, Chennai.	Academic Council Nominee	Online mode
10	Prof.Dr. M.Dhinakaran., Ph.D., Assistant Professor, Dept. of ECE, Government College of Engineering, Salem	Academic Council Nominee	Online mode
11	Smt. Sudha M Larsen and Toubro (L & T) Defence, Bangalore,	Industry Representative	Online mode
12	Mr. S. Muniraj Sankar Senior Specialist , HCL Technologies Ltd ,Chennai	Alumnus Member	Online mode

Date: 18.09.2021

Signature of the Chairman-BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & FC)
Adhiyamaan College of Engineering (Autonomous)

Mosur - \$35 109
Krannagiri (Dt), Tamii Nadu.

Department of ECE- 25th BOS Recommendations

Sem	Course Code	Course Name	Remarks (New course Introduced/ Contents incorporated in the
*/***	818ECE02	ARM System Architecture and	corresponding course) The following topics are included in the existing syllabus, • ARM Organization
VIII	010ECE02	applications	3-Stage Pipeline5-Stage Pipeline
VIII	818ECE03	Radar and Navigational Aids	The following topics are included in the existing syllabus, Introduction-Four Methods of Navigation
VIII	818ECE04	Parallel and Distributed Processing	Newly Introduced Course. The unit modules are as follows: • Introduction to distributed and parallel systems • Communication in distributed and parallel environment • Distributed operating systems • Distributed resource management • Fault tolerance and consensus
VIII	818ECE05	Compressive sensing	Newly Introduced Course. The unit modules are as follows Introduction to compressed sensing Sparsity and signal recovery Recovery algorithms Compressive sensing for wsn Applications of compressive sensing
VIII	818ECE06	MEMS and NEMS	Newly Introduced Course. The unit modules are as follows: Overview and introduction Mems fabrication technologies Micro sensors Micro actuators Essentials of nano scale systems/ structure
VIII	818ECE08	Satellite Communication	The following topics are included in the existing syllabus, DAMA Assignment Methods, Compression-encryption, Coding Schemes.
VIII	818ECE09	Microwave Integrated Circuits Design	Newly Introduced Course. The unit modules are as follows: Power dividers Filter design Analysis of transistor amplifier Oscillator design Diode mixer

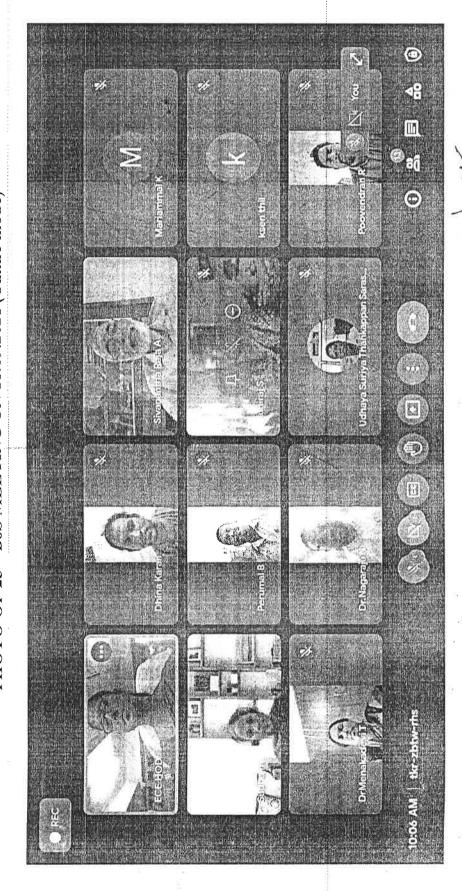
			Newly Introduced Course. The unit modules are as
	212FCF11		follows:
			Capacity of wireless channels
VIII		Advanced Wireless	Radio wave propagation
1 111		Communication	Space time block codes
		· .	Space time trellis codes
			Layered space time codes
			Newly Introduced Course. The unit modules are as
		•••	follows:
		non i distantant and	 Fundamentals of programmable DSPs
VIII	818ECE12	DSP Architecture and	Tms320c3x processor
		Programming	ADSP processors
			Advanced processors I
		:	Advanced processors II

Feedback obtained about curriculum and syllabus from stakeholders (Students, Faculty, Employers & Alumni) were also discussed and concluded.

BOS CHAIRMAN

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & FC)
Adhiyamaan College of Engineering (Autonomous)
Hosur - 635 109
Krishnagirl (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING [AUTONOMOUS], HOSUR-635109 FACULTY OF ELECTRONICS & COMMUNICATION ENGINEERING[UG&PG] PHOTO OF 25th BoS MEETING ON 18.09.2021 (Online mode)



Signature of Chairman -BoS

Adhiyamaan College of Engineering (Autonomous) Faculty of Electronics and Communication Engineering Chairman, Board of Studies Mosur - 634 \$09

Krishnegiri (Dt), Tamii Nadu.

Date: 18.09.2021

ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109

(Autonomous)

BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the Twenty-Fourth Meeting

Minutes of the 24th Meeting of the Board of Studies in Electronics & Communication Engineeringheld on 27.03.2021 at 11.00 A.M.

MEMBERS PRESENT

- 1. Dr. S. Sumathi, Chairman
- 2. Dr. T Menakadevi, Internal Member
- 3. Dr.R.Poovendran, Internal Member
- 4. Dr.N.Nagaraju, Internal Member
- 5. Prof.S. Vijay Murugan, Internal Member
- 6. Dr.T.S.UdhayaSuriya, Interdisciplinary Member, HoD/BME
- 7. Dr.B.Perumal, Interdisciplinary Member, Dept of EEE
- 8. Prof.Dr.A.Sivanantha Raja, University Nominee
- 9. Prof. Dr. K. Mariammal, Academic Council Nominee
- 10. Prof. Dr. M. Dhinakaran, Academic Council Nominee
- 11. Smt.Sudha M, Industry Representative
- 12. Mr. S. Munirajsankar, Alumnus Member
- 24.1 To consider the minutes of the 23rd Board of Studies meeting in Electronics and Communication Engineering held on 07.10.2020.
 - RESOLVED that the minutes of the 23rd Board of Studies meeting in Electronics and Communication Engineering held on 07.10.2020 have been considered.
- 24.2 To consider the Syllabi of 7th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018 2019.
 - **RESOLVED** that the Syllabi of 7th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018 2019be approved (Annexure I)
- 24.3 To consider the Syllabi of 5th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 2020.

RESOLVED that the Syllabi of 5th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved (Annexure II)

To consider the Syllabi of "Microprocessors and Microcontrollers" and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020.

RESOLVED that the Syllabi of "Microprocessors and Microcontrollers" and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved(Annexure III)

24.5 To consider the Syllabi of 3rd Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

RESOLVED that the Syllabi of 3rd Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure IV)

To consider the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3rd Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

RESOLVED that consider the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3rd Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure V)

24.7 To consider the Syllabus of "Communication Engineering" for 3rd Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

RESOLVED that the Syllabus of "Communication Engineering" for 3rd Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure VI)

To consider the Syllabi of 3rd Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021.

RESOLVEDthat the Syllabi of 3rd Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021be approved (Annexure VII)

24.9 To consider the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021-2022.

RESOLVEDthat consider the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021-2022 be approved (Annexure VIII)

24.10 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

RESOLVED the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineeringbe approved. (Annexure IX)

24.11 To consider the list of Examiners for the conduction of examinations (Practical &Theory) for the academic year 2021-2022.

RESOLVED that the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2021-2022 be approved. (Annexure X)

Date: 27.03.2021

Signature of Chairman -BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & F@)
Adhiyamaan College of Engineering (Autonomous)
Hosur - 635,109
Krishnagiri (Dt), Tamii Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Members Present for the Twenty Fourth Board of Studies Meeting held on 27.03.2021

Sl.No.	Name & Address of the Members	Category	Signature
1	Dr. S. Sumathi, Ph.D (VLSI)., Prof & Head, Dept. of ECE, ACE, Hosur.	BoS-Chairman	Jeeel
2	Dr. T.Menakadevi., M.Tech (VLSI), Ph.D., Professor, Dept. of ECE, ACE, Hosur.	Internal Member	- slul
3	Dr.R.Poovendran., M.E., (VLSI Design) AP, Dept. of ECE, ACE, Hosur.	Internal Member	An -
4	Prof.Dr. N.Nagaraju., Ph.D.,. Asst. Professor, Dept.of ECE, ACE, Hosur	Internal Member	N. Negarit
5	Prof.S.Vijay Murugan ,M.E., Asso.Professor , Dept. of ECE , ACE, Hosur.	Internal Member	S. Vijay muri
6	Dr.T.S.Udhaya Suriya Professor & Head Department of BME, ACE, Hosur.	Interdisciplinary Member	San
7	Prof.Dr.B.Perumal, Ph.D., Asso. Professor / Dept. of EEE Adhiyamaan College of Engineering, Hosur.	Interdisciplinary Member	Be
8	Prof.Dr.A Sivanantha Raja.,Ph.D Professor & Head ,Dept. of ECE, Alagappa Chettiar Government College of Engineering and Technology,Karaikudi	University Nominee	Online mode
9	Prof.Dr. K.Mariammal., Ph.D., Assistant Professor (Sr Gr), Department of Electronics Engineering, Madras Institute of Technology Anna University, Chennai.	Academic Council Nominee	Online mode
10	Prof.Dr. M.Dhinakaran.,Ph.D., Assistant Professor, Dept. of ECE, Government College of Engineering, Salem	Academic Council Nominee	Online mode
11	Smt. Sudha M Larsen and Toubro (L & T) Defence, Bangalore,	Industry Representative	Online mode
12	Mr. S. Muniraj Sankar Senior Specialist , HCL Technologies Ltd ,Chennai	Alumnus Member	Online mode

Date: 27.03.2021

Signature of the Chairman-BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UC 2 (C)
Adhiyamaen College of Engineering (Autonomous)
**Robur - 636 169
Kalehnagiri (Dt), Tamil Nadu.

Department of ECE- 24th BOS Recommendations

Sem	Course Code	Course Name	Remarks (New course Introduced/ Contents incorporated in the
			corresponding course)
		0 m	Newly Introduced Course. The unit modules are as follows:
· 20			Ad hoc networks – introduction and routing protocols
VII	718ECT01	Adhoc and Wireless Sensor Networks	• Sensor networks – introduction & architectures
	- L	s '	 Wsn networking concepts and protocols Sensor network security Sensor network platforms and tools
VII	718ECT02	Optical Communication	The following topics are included in the existing syllabus, SOURCES & DETECTORS (unit-3) coupling(unit-4)
VII	718ECT03	Antenna and Microwave Engineering	The existing syllabus has been reframed with the following unit modules Introduction to microwave systems and antennas Radiation mechanisms and design aspects Antenna arrays and applications Passive and active microwave devices Microwave design principles
VII	718ECT04	Embedded Systems	The following topics are included in the existing syllabus,
VII	718ECE01	Advanced Digital Signal Processing	The following topics are removed in the existing syllabus, Transmultiplexer filter banks (unit-3)
VII	718ECE02	RF System Design	The following topics are included in the existing syllabus, Design of RF oscillator using CAD (unit-5) PLL using CAD (unit-5)
VII	718ECE03	Multimedia Compression Techniques	Newly Introduced Course. The unit modules are as follows Multimedia components Text compression

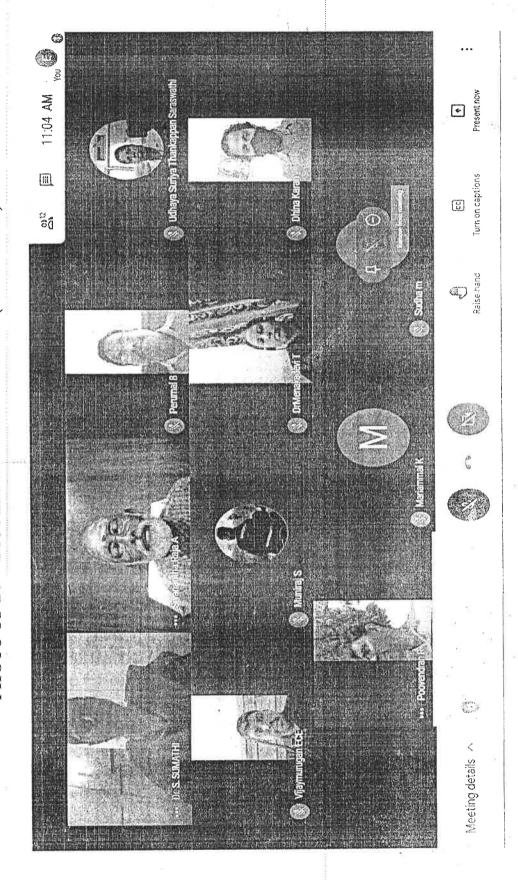
			Audio compression
		=	
		3 5	Image compression Video compression
			Video compression The writer adules are
			Newly Introduced Course. The unit modules are as follows: • Introduction
VII	718ECE04	Nano Technology	 Diversity in nano systems Metal nano particles and nano shells Evolving interfaces in nano Society and nano technology
VII	718ECE06)	Optical Networks	Newly Introduced Course. The unit modules are as follows: Introduction to optical networks and components Single and multi-hop networks Optical switching Optical access networks & metro networks Routing and optical multicasting
VII	718ECE07	Cognitive Radio	Newly Introduced Course. The unit modules are as follows. Introduction to software defined radio Sdr architecture Introduction to cognitive radios Cognitive radio architecture Next generation wireless networks
VII	718ECE08	Wireless Networks	The following topics are included in the existing syllabus, LTE Network Architecture OFDM in LTE
VII	718ECE11	Detection and Estimation Theory	Newly Introduced Course. The unit modules are as follows: Hypothesis testing Signal detection applications Random parameter estimation Minimum variance unbiased estimation Non-random parameter estimation
VII	718ECE12	CMOS Analog IC Design	Newly Introduced Course. The unit modules are as follows Introduction to analog ic design and current mirrors Amplifiers and feedback Frequency response of amplifiers and noise

	Operational amplifier stability and
	frequency compensation
	Switched capacitor circuits and PLLS
Feedback obtained about curricu	lum and syllabus from stakeholders (Students, Faculty, Employers &
Alumni) were also discussed and	concluded.

BOS CHAIRMAN

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & FC)
Adhiyamaan College of Engineering (Autonomous)
Hosur - 635 109
Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING [AUTONOMOUS], HOSUR-635109 FACULTY OF ELECTRONICS & COMMUNICATION ENGINEERING [UG&PG] PHOTO OF 24th BoS MEETING ON 27.03.2021 (Online mode)



Signature of Chairman –BoS

Chairman, Board of Studies
faculty of Electrosis and Communication Engine (10 % FC)
Adhiyaman College of Engineering (Autonomous)
Monut - 638 109
Khahnagari (Dt), Tamil Nadu.

Date: 27.03.2021

ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109

(Autonomous)

BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the Twenty-Third Meeting

Minutes of the 23rd Meeting of the Board of Studies in Electronics & Communication Engineering held on 07.10.2020 at 11.00 A.M.

MEMBERS PRESENT

- 1. Dr. S. Sumathi, Chairman
- 2. Dr. T Menakadevi, Internal Member
- 3. Prof.M.Sivakumar, Internal Member
- 4. Prof.Dr.R.Poovendran, Internal Member
- 5. Prof.Dr.N.Nagaraju, Internal Member
- 6. Prof.S. Vijay Murugan, Internal Member
- 7. Dr.T.S.Udhaya Suriya, Interdisciplinary Member, HoD/BME
- 8. Prof.Dr.A Sivanantha Raja, University Nominee
- 9. Prof.Dr. K.Mariammal, Academic Council Nominee
- 10. Prof.Dr. M.Dhinakaran, Academic Council Nominee
- 11. Smt. Sudha M, Industry Representative
- 12. Mr. S. Muniraj Sankar, Alumnus Member
- 23.1 To consider the minutes of the 23rd Board of Studies meeting in Electronics and Communication Engineering held on 22.06.2020.
 - **RESOLVED** that the minutes of the 23rd Board of Studies meeting in Electronics and Communication Engineering held on 22.06.2020 have been considered.
- 23.2 To consider the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017–2018.
 - **RESOLVED** that the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017–2018 be approved (Annexure I)
- 23.3 To consider the Syllabi of 7th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 2018.
 - **RESOLVED** that the Syllabi of 7th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 2018 be approved (Annexure II)

- 23.4 To consider the Syllabi of 6th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 2019.
 - **RESOLVED** that the Syllabi of 6th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 2019 be approved (Annexure III)
- 23.5 To consider the Syllabi of 5th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 2019.
 - **RESOLVED** that the Syllabi of 5th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 2019 be approved (Annexure IV)
- 23.6 To consider the Syllabi of "Microprocessors and Microcontrollers with Applications" and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 –2019.
 - **RESOLVED** that the Syllabi of "Microprocessors and Microcontrollers with Applications" and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 –2019 be approved (Annexure V)
- 23.7 To consider the Syllabi of 4th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 2020.
 - **RESOLVED** that the Syllabi of 4th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 2020 be approved (Annexure VI)
- 23.8 To consider the Syllabi of 3rd Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 2020.
 - **RESOLVED** that the Syllabi of 3rd Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 2020 be approved (Annexure VII)
- To consider the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3rd Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2019 2020.
 - **RESOLVED** that consider the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3rd Semester B.E. (Computer Science and Engineering/Information

Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved (Annexure VIII)

23.10 To consider the Syllabus of "Communication Engineering" for 3rd Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2019 – 2020.

RESOLVED the Syllabus of "Communication Engineering" for 3rd Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved. (Annexure IX)

23.11 To consider the Syllabi of "Electric Circuits and Electron Devices" and "Circuits and Devices Laboratory" for 2nd Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2020 – 2021.

RESOLVED the Syllabi of "Electric Circuits and Electron Devices" and "Circuits and Devices Laboratory" for 2nd Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2020 – 2021 be approved. (Annexure X)

23.12 To consider the Syllabi of 4th Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

RESOLVED that the Syllabi of 4th Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure XI)

23.13 To consider the Syllabi of 4th Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

RESOLVED that the Syllabi of 4th Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure XII)

23.14 To consider the Syllabi of 3rd Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

RESOLVED that the Syllabi of 3rd Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure XIII)

23.15 To consider the Syllabi of 3rd Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

RESOLVED that the Syllabi of 3rd Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure XIV)

23.16 To consider the Syllabi of 2nd Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021.

RESOLVED that the Syllabi of 2nd Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021 be approved (Annexure XV)

23.17 To consider the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2020-2021.

RESOLVED that the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2020-2021 be approved (Annexure XVI)

23.18 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

RESOLVED the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved. (Annexure XVII)

To consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2020-2021.

RESOLVED that the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2020-2021 be approved. (Annexure XVIII)

Date: 07.10.2020

Signature of Chairman –BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & FC)
Adhiyamaan College of Engineering (Autonomous)
Hosey - 636 100
Rotenhageri Cott. Tamis Medu.

ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Members Present for the Twenty third Board of Studies Meeting held on 07.10.2020

Sl.No.	Name & Address of the Members	Category	
1	Dr. S. Sumathi, Ph.D (VLSI)., Prof & Head, Dept. of ECE, ACE, Hosur.	BoS-Chairman	Jelee
2 .	Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D., Professor, Dept. of ECE, ACE, Hosur.	Internal Member	Tolletil
3	Mr.M.Sivakumar,M.E(Communication Systems)., AP, Dept. of ECE, ACE, Hosur.	Internal Member	#
4	Dr.R.Poovendran., M.E., (VLSI Design) AP, Dept. of ECE, ACE, Hosur.	Internal Member	Mn
5	Prof.Dr. N.Nagaraju., Ph.D., Asst. Professor, Dept.of ECE, ACE, Hosur	Internal Member	N. Naporas
6	Prof.S.Vijay Murugan ,M.E., Asso.Professor , Dept. of ECE , ACE, Hosur.	Internal Member	S. Viloy Mu
7	Dr.T.S.Udhaya Suriya Professor & Head Department of BME, ACE, Hosur.	Interdisciplinary Member	Land
8	Prof.Dr.A Sivanantha Raja.,Ph.D Professor & Head ,Dept. of ECE, Alagappa Chettiar Government College of Engineering and Technology,Karaikudi	University Nominee	Online mode
9	Prof.Dr. K.Mariammal., Ph.D., Assistant Professor (Sr Gr), Department of Electronics Engineering, Madras Institute of Technology Anna University, Chennai.	Academic Council Nominee	Online mode
.10	Prof.Dr. M.Dhinakaran., Ph.D., Assistant Professor, Dept. of ECE, Government College of Engineering, Salem	Academic Council Nominee	Online mode
11	Smt. Sudha M Larsen and Toubro (L & T) Defence, Bangalore,	Industry Representative	Online mode
12	Mr. S. Muniraj Sankar Senior Specialist, HCL Technologies Ltd, Chennai	Alumnus Member	Online mode

Date: 07.10.2020

Signature of the Chairman-BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (80 & FC)
Adhiyamaan College of Engineering (Autonomous)
Hosur 635 109

Krishnagiri (Dt), Tamil Nadu.

Department of ECE- 23rd BOS Recommendations

Sem	Course Code	Course Name	Remarks (New course Introduced/ Contents incorporated in the
			corresponding course)
VI	618ECT02	VLSI Design	The following topics are included in the existing syllabus, i) Noise Margin, Rise time and Fall time. (Unit-2) ii) Adhoc testing, Scan Design, BIST, IDDQ testing(unit-4) iii) The following topics are removed in the
			existing syllabus, iv) Large FETs(unit-3)
VI	618ECT03	Cellular and Mobile Communication	The following topics are included in the existing syllabus, i) MIMO systems(unit-3) ii) 4G Technologies: LTE (unit-5) iii) The following topics are in the existing syllabus, iv) Basic combining methods(unit-3) v) AMPS (unit-5)
VI	618ECE01	Digital Image Processing	The following topics are included in the existing syllabus, i) Relationship between pixels(unit-1) ii) Morphological processing - dilation - erosion(unit-4) iii) Huffman coding (unit-5) The following topics are removed in the existing syllabus, i) Geometric transformations: spatial transformations-
dia.	dansta		Gray- Level interpolation. (Unit-3)
VI	618ECE02	Robotics Engineering	 Newly Introduced Course. The unit modules are as follows: Introduction Elements of robots-joints, links, actuators and sensors End effectors and robot controls Robot cell design and applications Micro/Nano Robotics System
VI	618ECE03	Digital System Design using VHDL	The following topics are included in the existing syllabus i) Logic block, routing architecture and constraints(unit-1) Binary Multiplier, Dice game(unit-2)
VI	618ECE04	Information Theory Coding	The following topics are included in the existing syllabus, i) Image and Video Formats(unit-3) Text books reference is changed
VI	618ECE05	Soft Computing and Applications	Newly Introduced Course. The unit modules are as follows: Introduction To Soft Computing And Neural Networks Genetic Algorithms

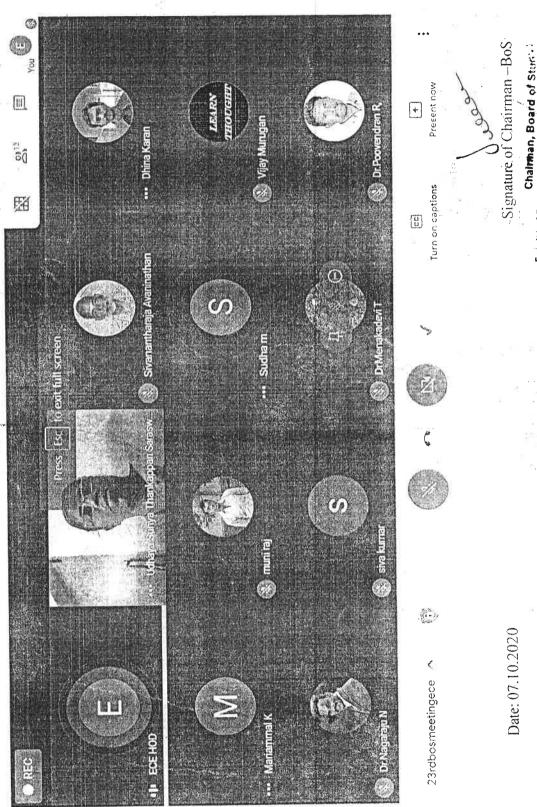
VI 618ECP08 VLSI Design Laboratory VLSI Design Laboratory Analog and Digital Communication Systems Laboratory Peedback obtained about curriculum and syllabus from stakeholders (Students, Faculty, Employers Eachland) Newly Introduced Course. The unit modules are as follows: Nature Of Speech Signal Time Domain Methods For Speech Processing Prequency Domain Methods For Speech Processing Linear Predictive Coding Of Speech Homomorphic Speech Analysis NNOVATIVE PROJECTS: Design and implementation of a traffic light controller using VHDL, Design a simulation of finite state machine(fsm) using VHDL, Posign a simulation of simple ALU USING VHDL. NNOVATIVE PROJECTS: Communication System Design using MATLAB Segin Delta Modulation using SIMULINK, Design Shift keying Techniques using MATLAB				3 A
VI 618ECE06 Speech Processing Speech Processing Nature Of Speech Signal Time Domain Methods For Speech Processing Frequency Domain Methods For Speech Processing Linear Predictive Coding Of Speech Homomorphic Speech Analysis INNOVATIVE PROJECTS: Design and implementation of a traffic light controller using VHDL, DC motor interfacing, LED interfacing, Design a simulation of finite state machine(fsm) using VHDL, Design a simulation of simple ALU USING VHDL. VI 618ECP07 Analog and Digital Communication System Design using MATLAB (Signal Generation and Interpretation), Pulse Code Modulation using MATLAB, Design Amplitude and Frequency modulation using SIMULINK, Design Delta Modulation using SIMULINK, Design Design Techniques using MATLAB	,			Fuzzy LogicNeuro-Fuzzy Modeling
VI 618ECP08 VLSI Design Laboratory VLSI Design Laboratory • Design and implementation of a traffic light controller using VHDL, • DC motor interfacing, • LED interfacing, • Design a simulation of finite state machine(fsm) using VHDL, • Design a simulation of simple ALU USING VHDL. • INNOVATIVE PROJECTS: • Communication System Design using MATLAB (Signal Generation and Interpretation), • Pulse Code Modulation using MATLAB, • Design Amplitude and Frequency modulation using SIMULINK, • Design Delta Modulation using SIMULINK, • Design Shift keying Techniques using MATLAB	VI	618ECE06)	Speech Processing	 Nature Of Speech Signal Time Domain Methods For Speech Processing Frequency Domain Methods For Speech Processing Linear Predictive Coding Of Speech Homomorphic Speech Analysis
 VI 618ECP07 Analog and Digital Communication Systems Laboratory Design Amplitude and Frequency modulation using SIMULINK, Design Delta Modulation using SIMULINK, Design Shift keying Techniques using MATLAB 	VI	618ECP08	_	 Design and implementation of a traffic light controller using VHDL, DC motor interfacing, LED interfacing, Design a simulation of finite state machine(fsm) using VHDL, Design a simulation of simple ALU USING
Feedback obtained about curriculum and syllabus from stakeholders (Students, Faculty, Employers	· .		Communication Systems Laboratory	 Communication System Design using MATLAB (Signal Generation and Interpretation), Pulse Code Modulation using MATLAB, Design Amplitude and Frequency modulation using SIMULINK, Design Delta Modulation using SIMULINK, Design Shift keying Techniques using MATLAB
		Feedback obtained	about curriculum and sy	yllabus from stakeholders (Students, Faculty, Employers

& Alumni) were also discussed and concluded.

BOS CHAIRMAN

Chairman, Priard of Studies
Faculty of Electronics and Communication Engineering (UG & PC)
Adhiyamaan Collyge of Engineering (Autonomous)
Hosur - 535 109
Krishnagiri (Dt), Tamii Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUK-635109 FACULTY OF ELECTRONICS & COMMUNICATION ENGINEERING[UG&PG] PHOTO OF 23rd BoS MEETING ON 07.10.2020 (Online mode)



Chairman, Board of Sturce:
Adhlyamaan College of Engineering (Autenomous)
Mosur - 635 109
Khahhagiri (bt), Tamil Nadu,

ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109

(Autonomous)

BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the Twenty-second Meeting

Minutes of the 22nd Meeting of the Board of Studies in Electronics & Communication Engineering held on 02.11.2019 at 10.30 A.M.

MEMBERS PRESENT

- 1. Dr. S. Sumathi, Chairman
- 2. Dr. K.A.Dattatreya, Internal Member
- 3. Dr. T Menakadevi, Internal Member
- 4. Prof.M.Sivakumar, Internal Member
- 5. Prof.R.Poovendran, Internal Member
- 6. Dr.T.S.Udhaya Suriya, Interdisciplinary Member, HoD/BME
- 7. Prof.Dr.Narayanappa, Interdisciplinary Member, HoD/EEE
- 8. Prof.Dr.N.Ramadass, Special Invitee
- 9. Prof.Dr.D.Seshachalam, Academic Council Nominee
- 10. Prof.Dr.G.Kavitha, Academic Council Nominee
- 11. Smt.N.Dhivya,Industry Representative
- 12. Shri.R. Chandrashekar, Alumnus Member
- 22.1 To consider the minutes of the 21st Board of Studies meeting in Electronics and Communication Engineering held on 27.04.2019.
 - **RESOLVED** that the minutes of the 21st Board of Studies meeting in Electronics and Communication Engineering held on 27.04.2019 have been considered.
- 22.2 To consider the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 2017.
 - **RESOLVED** that the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 2017 be approved (Annexure I)
- 22.3 To consider the Syllabi of 6th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017 2018.
 - **RESOLVED** that the Syllabi of 6th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017 2018 be approved (Annexure II)

22.10 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

RESOLVED the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved. (Annexure IX)

22.11 To consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2019-2020.

RESOLVED that the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2019-2020 be approved. (Annexure X)

Date: 02.11.2019

Signature of Chairman -BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & FC)
Adhiyamaan College of Engineering (Autonomous)
Hour J. J. J. Krishnesiri (Dt. Tenn Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Members Present for the Twenty second Board of Studies Meeting held on 02.11.2019

Sl.No.	Name & Address of the Member	s	Category	Signature
1	Dr. S. Sumathi, Ph.D (VLSI)., Prof & Head, Dept. of ECE, ACE, Hosur.	*1	BoS-Chairman	Juse
2	Dr. K.A.Dattatreya, M.E (Electronics), MBA, Professor, Dept. of ECE, ACE, Hosur.	, Ph.D	Internal Member	Ma
3	Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D., Professor, Dept. of ECE, ACE, Hosur.	*	Internal Member	July
4	Mr.M.Sivakumar,M.E(Communication System AP, Dept. of ECE, ACE, Hosur.	ms).,	Internal Member	7
5	Mr.R.Poovendran.,M.E.,(VLSI Design) AP, Dept. of ECE, ACE, Hosur.		Internal Member	Hur
6	Dr.T.S.Udhaya Suriya Professor & Head Department of BME, ACE, Hosur.		Interdisciplinary Member	Len
7	Dr.Narayanappa, M.E., Ph.D., Professor & Head, Department of EEE, ACE, Hosur.	**. ±	Interdisciplinary Member	Brat
8	Prof.Dr.N.Ramadass, M.E., Ph.D.,(Communi Systems) Professor/Department of ECE, CEG Campus, Anna University, Chennai.	cation	Special Invitee	de
9	Prof.Dr. D. Seshachalam(Embedded syste Prof & Head/Dept. of ECE BMS college of Engineering, Bangalore.	ems)	Academic Council Nominee	Sta
10 =	Prof.Dr.G.Kavithaa(Communication systems Assistant Professor/ Dept. of ECE, Government College of Engineering, Salem.)	Academic Council Nominee	C. with
11	Smt.N.Dhivya,(Electronics) Technical Lead,HCL technologies, Jigani, Bangalore.		Industry Representative	N. Dhy
12	ShriR.Chandrashekar, HR,QAD Department TVS- Hosur	426	Alumnus Member	

Date: 02.11.2019

Signature of the Chairman-BoS

Chairman, Board of Studies

Faculty of Electronics and Communication Engineering (90.2.00)

Adhiyamaan College of Engineering (Autonomous)

Hosur - 635 109

Krishnagiri (Dt), Tamil Nadu.

Department of ECE- 22st BOS Recommendations

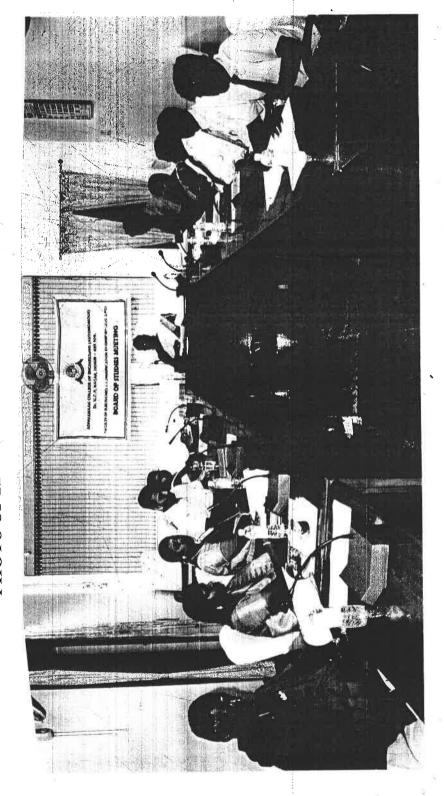
Sem	Course Code	Course Name	Remarks (New course Introduced/ Contents incorporated in the		
			corresponding course) The following topics are included in the existing		
IV	418ECT03	Linear Integrated Circuits	syllabus, i) Astable and Mono stable Multivibrator using 555 NE Timer		
			The following topics are included in the existing syllabus,		
IV	418ECT04	Analog Electronics -II	 i) Analysis of voltage series and current series Practical feedback amplifiers circuits. ii) Analysis of single tuned amplifier and its 		
			bandwidth-Analysis of double tuned amplifier and its bandwidth		
IV	418ECP08	Linear Integrated Circuit Laboratory	The following Innovative Projects are included in the existing syllabus, i) Automatic Street Light using 555 Timer, Rainfall Detector Alarm using 555 Timer & Rain Sensor, Automatic LED Blinking Circuit using 555 Timer IC – LED Flasher, Automatic Predefined Time Lamp Turn On Project		
IV ,	418ECP09 Analog Electronics-II Laboratory		The following Innovative Projects are included in the existing syllabus, i) Water level alarm, USB mobile charger circuit, Bike turning signal circuit, 555 timer IC testing circuit, Dancing bike colour LED light circuit		
IV	418ECE02 Consumer Electronics syllabus,		The following topics are included in the existing syllabus, i) Home Audio systems		
IV	418ECE05	PCB Design	The following topics are included in the existing syllabus, i) 3-D printing, Laser printing		

Feedback obtained about curriculum and syllabus from stakeholders (Students, Faculty, Employers, Alumni) were also discussed and concluded.

BOS CHAIRMAN

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & FC)
Adhiyamaan Collage of Engineering (Autonomous) Hosur - 635 109 Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING [AUTONOMOUS], HOSUR-635109 FACULTY OF ELECTRONICS & COMMUNICATION ENGINEERING[UG&PG] PHOTO OF 22nd BoS MEETING ON 02.11.2019



Prof.M.Sivakumar, Dr.T.S.Udhaya Suriya, Dr.T.Menakadevi, Shri.R.Chandrashekar, Smt.N.Dhivya, Dr.S.Sumathi Prof.Dr.N.Ramadass, Dr.G.Kavithaa, Dr. K.A.Dattatreya, Dr.Narayanappa, Prof.R.Poovendran Clockwise:

ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109

(Autonomous)

BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the Twenty-first Meeting

Minutes of the 21st Meeting of the Board of Studies in Electronics & Communication Engineering held on 27.04.2019 at 10.00 A.M.

MEMBERS PRESENT

- 1. Dr.S.Sumathi, Chairman
- 2. Dr.K.A.Dattatreya, Internal Member
- 3. Dr.T Menakadevi, Internal Member
- 4. Prof.M.Sivakumar, Internal Member
- 5. Prof.R.Poovendran, Internal Member
- 6. Dr.T.S.Udhaya Suriya, Interdisciplinary Member, HoD/BME
- 7. Prof.Dr.S. Jayakumar, Interdisciplinary Member, Asst. Prof/CHEM. ENGG.
- 8. Prof.Dr.N.Ramadass, University Nominee
- 9. Prof.Dr.G.Kavitha, Academic Council Nominee
- 10. Smt.N.Dhivya,Industry Representative
- 11. Shri.R.Chandrashekar, Alumnus Member
- 21.1 To consider the minutes of the 20th Board of Studies meeting in Electronics and Communication Engineering held on 27.10.2018.
 - **RESOLVED** that the minutes of the 20th Board of Studies meeting in Electronics and Communication Engineering held on 27.10.2018have been considered.
- 21.2 To consider the Syllabi of 7th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 2017.
 - **RESOLVED** that the Syllabi of 7th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 2017 be approved (Annexure I)
- 21.3 To consider the Syllabi of 5th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 2018.
 - **RESOLVED** that the Syllabi of 5th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 2018 be approved (Annexure II)

To consider the Syllabi of 3rd Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019.

RESOLVED the Syllabi of 3rd Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019 be approved (Annexure VIII)

21.10 To consider the Syllabi of 3rd Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018-2019.

RESOLVED that the Syllabi of 3rd Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018-2019 be approved (Annexure IX)

21.11 To consider the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2019-2020.

RESOLVED the Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2019-2020 be approved. (Annexure X)

21.12 To consider the Syllabi of 1st Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students to be admitted in 2019-2020

RESOLVED that the Syllabi of 1st Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students to be admitted in 2019-2020 be approved. (Annexure XI)

21.13 To consider the list of examiners for the conduction of examinations (Practical & Theory) for the academic year 2019-2020.

RESOLVED that the list of examiners for the conduction of examinations (Practical & Theory) for the academic year 2019-2020 be approved (Annexure XII)

Date: 27.04.2019

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Signature Chairman -BoS

Chairman, Donrd of Studies

Faculty of Electrosics and Communication Engineering (Autonomous)

Adhiyamaan College of Engineering (Autonomous)

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ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Members Present for the Twenty-first Board of Studies Meeting held on 27.04.2019

Sl.No.	Name & Address of the Members	Category	Signature
1.	Dr. S. Sumathi, Ph.D (VLSI)., Prof & Head, Dept. of ECE, ACE, Hosur.	BoS-Chairman	Joese
. 2 , _	Dr. K.A.Dattatreya, M.E (Electronics), MBA, Ph.D Professor, Dept. of ECE, ACE, Hosur.	Internal Member	Mor
3	Dr. T.Menakadevi., M.Tech (VLSI), Ph.D., Professor, Dept. of ECE, ACE, Hosur.	Internal Member	- llele
4	Mr.M.Sivakumar, M.E(Communication Systems)., AP, Dept. of ECE, ACE, Hosur.	Internal Member	**
5	Mr.R.Poovendran.,M.E.,(VLSI Design) AP, Dept. of ECE, ACE, Hosur.	Internal Member	Mr
6	Dr.T.S.Udhaya Suriya Professor & Head Department of BME, ACE, Hosur.	Interdisciplinary Member	Line
7	Dr.S.Jayakumar, M.E., Ph.D., (Applied Electronics) Assistant Professor/Dept. of Chemical Engineering, Adhiyamaan College of Engineering, Hosur.	Interdisciplinary Member	A. Man
8	Prof.Dr.N.Ramadass, M.E., Ph.D., (Communication Systems) Associate Professor/Department of ECE, CEG Campus, Anna University, Chennai.	University Nominee	de
9	Prof.Dr. D. Seshachalam(Embedded systems) Prof & Head/Dept. of ECE BMS college of Engineering, Bangalore.	Academic Council Nominee	-AB-
10	Prof.Dr.G.Kavitha(Communication systems) Assistant Professor/ Dept. of ECE, Government College of Engineering, Salem.	Academic Council Nominee	b. W. in
11	Smt.N.Dhivya,(Electronics) Technical Lead,HCL technologies, Jigani, Bangalore.	Industry Representative	2.D/2/ 2/4/19
12	ShriR.Chandrashekar, HR,QAD Department TVS- Hosur	Alumnus Member	de la

Date: 27.04.2019

Signature of the Chairman-BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UC 1.FC)
Adhiyamaan College of Engineering (Autonomous)
Hosur - 635 109
Krishnagiri (Dt), Tamil Nadu.

Department of ECE- 21st BOS Recommendations

Sem	Course Code	Course Name	Remarks
Jem :	Course Coue	Course Name	(New course Introduced/ Contents incorporated in the corresponding course)
			Topics newly added:
III	318ECT04	Analog Electronics - I	
		Timatog Electronics - 1	The state of the s
		Analog Electronics –I	Thermistor Compensation
Ш	318ECP08	Laboratory	Experiment newly added:
		Laboratory	Design of voltage regulator using BJT Experiment newly added:
		n•	i) Study of RAM as a storage device
Ш	318ECP09	Digital Electronics Laboratory	ii) Implementation of Ring counter and Johnson counter using MSI devices
		- grow Electionies Elaboratory	Experiment removed:
			Design of Adder, Subtractor, Multiplexer,
		- i	Ripple counter and Shift register using VHDL
		M.E- COMMUNICA	TION SYSTEMS
	,41		The following unit modules are included:
	*	,	Unit-3 EMI/EMC STANDARDS AND
1			MEASUREMENTS
);			Civilian standards - FCC,CISPR,IEC,EN, Military
			standards - MIL STD 461D/462, EMI Tes
-			Instruments /Systems, EMI Shielded Chamber
		-	Open Area Test Site, TEM Cell.
			Sensors/Injectors/Couplers, Test beds for ESD and
		- E-	EFT, Military Test Method and Procedures (462)
		1	Unit- 5
		2 to	PCB Traces Cross Talk, Impedance Control, Power
	. 3771	n 'e	Distribution Decoupling, Zoning, Motherboard
	2	Electromagnetic Interference	Designs and Propagation Delay Performance
III	318COE09	and Compatibility in System	Models.
		Design	The following unit modulesare removed:
		. 3	Unit -3EMC DESIGN OF PCBS
			Component selection and mounting; PCB trace
		97 H	impedance; Routing; Cross talk control; Power
		·	distribution decoupling; Zoning; Grounding; VIAs
		1	connection; Terminations.
	•		Unit- 5 EMI MEASUREMENTS AND
			STANDARDS
1			Open area test site; TEM cell; EMI test shielded
			chamber and shielded ferrijte lined anechoic
			chamber; Tx /Rx Antennas, Sensors, Injectors /
į		10 A	Couplers, and coupling factors; EMI Rx and
			spectrum analyzer; Civilian standards-CISPR,
laadh	nak abtained - h	out our mioulum and a Hall C	FCC, IEC, EN; Military standards-MIL461E/462. a stakeholders (Students, Faculty, Employers &

Alumni) were also discussed and concluded.

BOS CHAIRMAN

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & FC)
Adhiyamaan College of Engineering (Autonomous)
Hosur - 635 109
Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING [AUTONOMOUS], HOSUR-635109 FACULTY OF ELECTRONICS & COMMUNICATION ENGINEERING[UG&PG] PHOTO OF 21st BoS MEETING ON 27.04.2019



Clockwise:

Prof.R.Poovendran, Prof.M.Sivakumar, Dr.T.S.Udhaya Suriya, Prof.Dr.N.Ramadass, Dr.S.Sumathi, Shri.R.Chandrashekar, Dr. K.A.Dattatreya, Dr.S.Jayakumar, Dr.T.Menakadevi.

ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109 (Autonomous) BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the Twentieth Meeting

Minutes of the 20th Meeting of the Board of Studies in Electronics & Communication Engineering held on 27.10.2018 at 10.00 A.M.

MEMBERS PRESENT

- 1. Dr. S. Sumathi, Chairman
- 2. Dr. K.A.Dattatreya, Internal Member
- 3. Dr. T Menakadevi, Internal Member
- 4. Prof.M.Sivakumar, Internal Member
- 5. Prof.R.Poovendran, Internal Member
- 6. Dr.T.S.Udhaya Suriya, Interdisciplinary Member, HoD/BME
- 7. Prof.Dr.S.Jayakumar, Interdisciplinary Member, Asst. Prof/B. Tech CHEM
- 8. Prof.Dr.N.Ramadass, University Nominee
- 9. Prof.Dr. D. Seshachalam, Academic Council Nominee
- 10. Prof.Dr.G.Kavitha, Academic Council Nominee
- 11. Smt.N.Dhivya,Industry Representative
- 12. Shri..R.Chandrashekar, Alumnus Member
- 20.1 To consider the minutes of the 19th Board of Studies meeting in Electronics and Communication Engineering held on 21.04.2018.
 - **RESOLVED** that the minutes of the 19th Board of Studies meeting in Electronics and Communication Engineering held on 21.04.2018 have been considered.
- 20.2 To consider the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 for the batch of students admitted in 2015 2016.
 - **RESOLVED** that the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 for the batch of students admitted in 2015 2016 be approved (Annexure I)

To consider the Syllabi of 2nd Semester M.E. (Communication Systems) programme under 20.9 the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019.

RESOLVED that the Syllabi of 2nd Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019 be approved (Annexure VIII)

20.10 To consider the Syllabi of 2nd Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018-2019.

RESOLVED the Syllabi of 2nd Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018-2019 be approved. (Annexure IX)

To consider the resolution pertaining to regulation -2018(CBCS).

- a) IA (Internal assessment) & EA (External Assessment) marks ratio.
- b) Continuous assessment (IA) Marks Allocation.

c) End Assessment Question Paper Pattern.

d) One Credit Courses over and above the total credits to be earned.

RESOLVED that the resolution pertaining to regulation -2018(CBCS).

- a) IA (Internal assessment) & EA (External Assessment) marks ratio.
- b) Continuous assessment (IA) Marks Allocation.
- c) End Assessment Question Paper Pattern.
- d) One Credit Courses over and above the total credits to be earned. be approved. (Annexure X)

20.12 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

RESOLVED that the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering. be approved (Annexure XI)

Date: 27.10.2018

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its or 118 Signature of Chairman -BoS Chairman, Board of

Faculty of Electronics and Communication Language Adhiyamaan College of Engineering (Autonomous)

Hosur - 635 109 Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Members Present for the Twentieth Board of Studies Meeting held on 27.10.2018

Sl.No.	Name & Address of the Mem	bers	Category	Signature
1	Dr. S. Sumathi, Ph.D (VLSI)., Prof & Head, Dept. of ECE, ACE, Hosur		BoS-Chairman	Jusen
2	Dr. K.A.Dattatreya, M.E (Electronics), M Professor, Dept. of ECE, ACE, Hosur.	Internal Member	Ma	
3	Dr. T.Menakadevi., M.Tech (VLSI), Ph.D. Professor, Dept. of ECE, ACE, Hosur.	,	Internal Member	A SURIE
4	Mr.M.Sivakumar,M.E(Communication S AP, Dept. of ECE, ACE, Hosur.	ystems).,	Internal Member	1 .
5	Mr.R.Poovendran., M.E., (VLSI Design) AP, Dept. of ECE, ACE, Hosur.	5	Internal Member	Mr
6	Dr.T.S.Udhaya Suriya Professor & Head Department of BME, ACE, Hosur.	**************************************	Interdisciplinary Member	Jam
7	Dr.S.Jayakumar, M.E., Ph.D., (Applied Elect Assistant Professor/Dept. of Chemical Engi Adhiyamaan College of Engineering, Hosur.	neering,	Interdisciplinary Member	d. Hai
8	Prof.Dr.N.Ramadass, M.E., Ph.D., (Commun Associate Professor/Department of ECE, CEG Campus, Anna University, Chennai.	ication Systems)	University Nominee	In 10.
9	Prof.Dr. D. Seshachalam(Embedded syst Prof & Head/Dept. of ECE BMS college of Engineering, Bangalore.		Academic Council Nominee	-AB-
10	Prof.Dr.G.Kavitha(Communication systems) Assistant Professor/ Dept. of ECE, Government College of Engineering, Salem.		Academic Council Nominee	C. S. W.
11	Smt.N.Dhivya, (Electronics) Technical Lead, HCL technologies, Gigeni, Bangalore.	ř	Industry Representative	N. Ding
12	ShriR.Chandrashekar, HR,QAD Department TVS- Hosur	Section 2	Alumnus Member	Jan 24/10/10

Date: 27.10.2018

Signature of the Chairman-BoS

Chairman, Board of Studies

Chairman, Board of Studies

Faculty of Electronics and Communication Engineering (UG & FC)

Adhiyamaan College of Engineering (Autonomous)

Adhiyamaan College of Engineering (Autonomous) n Conege of Engineering (1997) Hosur - 635 109 Krishnagiri (Dt), Tamii Nadu.

Sem	Course Code	Name of the course	Remarks (New course Introduced/contents incorporated in the corresponding course)
erie i		-	Newly Introduced Course. The unit
			modules are as follows: Introduction to random signal processing
VIII	815ECE05	Statistical Signal Processing	 Spectral Estimation Linear Estimation and Prediction Adaptive Filters
		# #	Application Overview-speech Processing
	- Abo.		Newly Introduced Course. The unit
1.5 F			modules are as follows:
VIII	815ECE07	Wavelets and its applications	 Fundamentals Multi Resolution Analysis Continuous Wavelet Transforms Discrete Wavelet Transform
1			Applications
			Newly Introduced Course. The unit
	1		modules are as follows:
			Introduction to ASIC, CMOS Logic
			and ASIC Library
			Programmable ASICS, ASIC Lagric College
		LOTO D	Programmable ASIC Logic Cells
VIII	815ECE08	ASIC Design	and Programmable ASIC I/O
		:	Cells Programmable ASIC Architecture
			Logic Synthesis, Simulation and Testing
		*	 ASIC Construction, floor planning,
			placement and routing.
		S in a	Newly Introduced Course.
VIII	815ECE09	Satellite Communication	
			Newly Introduced Course. The unit
		2	modules are as follows:
		- 1	 Power Dissipation in CMOS
VIII	815ECE11	Low Power VLSI Design	Circuits
ATI	ULUL GETT	S C C C C C C C C C C C C C C C C C C C	Power Optimization Power Circuits
		- 1	Design of Low Power Circuits
			Power Estimation Section Design
			Synthesis and Software Design
		M.E – VLSI I	DESIGN
	5.		The following unit module is included: • TEST GENERATION (Unit-2)
			Faults in Digital circuits-failures
		PR. 41	and faults. Algorithms and Representations - Redundancy
II	218VLT02	Testing of VLSI circuits	Identification (RID) - Test
		6 " "	generation for combinational logic
		£. **	circuits-combinational ATPG-
			Boolean Difference Method-D-
T.	1.10		DOV.14

	2 8 7 7 8		,	Algorithm-PODEM - FAN Algorithm
				The following unit module is removed: • TEST GENERATION FOR COMBINATIONAL AND SEQUENTIAL CIRCUITS Test generation for combinational logic circuits - Testable combinational logic circuit design Test generation for sequential circuits - design of testable sequential circuits.
eed bac	ck obtained abo	ut curriculum an	d syllabus fr	om stakeholders (Students, Faculty,

BOS CHAIRMAN

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & FC)
Authyamaan College of Engineering (Autonomous)
Hosur - 635 109
Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING [AUTONOMOUS], HOSUR-635109 FACULTY OF ELECTRONICS & COMMUNICATION ENGINEERING[UG&PG] PHOTO OF 20th BoS MEETING ON 27.10.2018



Prof.M.Sivakumar, Dr.T.Menakadevi, Dr.T.S.Udhaya Suriya, Prof.Dr.N.Ramadass, Dr.S.Sumathi, Prof.Dr.G.Kavitha, Clockwise:

Smt.N.Dhivya, Dr.S.Jayakumar, Mr.R.Poovendran.

Faculty of Electronics and Communication Engineering (Autonomous)
Adhiyamaan College of Engineering Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING(AUTONOMOUS), HOSUR-635 109 BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the nineteenth Meeting

Minutes of the 19th Meeting of the Board of Studies in Electronics & Communication Engineering held on 21.04.2018 at 10.00 A.M.

MEMBERS PRESENT

- 1. Dr. S. Sumathi, Chairman
- 2. Dr. K.A.Dattatreya, Internal Member
- 3. Dr. T Menakadevi, Internal Member
- 4. Prof.M.Sivakumar, Internal Member
- 5. Prof.S.Chidambaram, Internal Member
- 6. Dr.E.Saravanakumar, , Interdisciplinary Member
- 7. Dr.T.S.Udhaya Suriya, Interdisciplinary Member, HoD/BME
- 8. Dr.L.Ganesan ,University Nominee
- 9. Dr.G.Geetha, Academic Council Nominee
- 10. Dr.K.J.Shanthi, Academic Council Nominee
- 11. Shri.J.Ravi Kumar ,Industry Representative
- 12. Shri.N.Thandayuthapani, Alumnus Member
- 19.1 To consider the minutes of the Eighteenth Board of Studies meeting in Electronics and Communication Engineering held on 28.10.2017.
 - **RESOLVED** that the minutes of the Eighteenth Board of Studies meeting in Electronics and Communication Engineering held on 28.10.2017 have been considered.
- 19.2 To consider the Syllabi of 7th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 for the batch of students admitted in 2015 2016.
 - **RESOLVED** that the Syllabi of 7^{th} Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 for the batch of students admitted in 2015-2016 be approved (Annexure I)
- 19.3 To consider the Syllabi of 5th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 2017.
 - **RESOLVED** that To consider the Syllabi of 5^{th} Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 2017 be approved (Annexure II)

19.4 To consider the Syllabi of "Microprocessors and Microcontrollers with Applications" and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E.(Compute "Microprocessors and Engineering) programme under the Regulations 2015(CBCS) for the batch students admitted in 2016 – 2017.

RESOLVED the Syllabi of "Microprocessors and Microcontrollers with Application and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E.(Comput Science and Engineering) programme under the Regulations 2015(CBCS) for the batch students admitted in 2016 – 2017 be approved.(Annexure III)

19.5 To consider the Syllabi of "Communication Engineering", "Embedded System Design and "Advanced Microprocessor Laboratory" for 5th Semester B.Tech.(Information Technology) programme under the Regulations 2015 (CBCS) for the batch of student admitted in 2016 – 2017.

RESOLVED that the Syllabi of "Communication Engineering", "Embedded System Design", and "Advanced Microprocessor Laboratory" for 5th Semest B.Tech.(Information Technology) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 – 2017 be approved.(Annexure IV)

19.6 To consider the Syllabi of 3rd Semester B.E. (Electronics and Communicative Engineering) programme under the Regulations 2015(CBCS) for the batch of studest admitted in 2017–2018.

RESOLVED that the Syllabi of 3rd Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of studes admitted in 2017–2018 be approved. (Annexure V)

19.7 To consider the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" and "Semester B.E. (Computer Science and Engineering) programme under the Regulation 2015 (CBCS) for the batch of students admitted in 2017 – 2018.

RESOLVED that the Syllabi of "Digital Electronics" and "Digital Electron Laboratory" for 3rd Semester B.E. (Computer Science and Engineering) programme un the Regulation 2015 (CBCS) for the batch of students admitted in 2017 – 2018 approved (Annexure VI)

19.8 To consider the Syllabi of "Digital Principles and Systems Design" and "Dig Laboratory" for 3rd Semester B.Tech.(Information Technology) programme under Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018.

RESOLVED that the Syllabi of "Digital Principles and Systems Design" and "Dig Laboratory" for 3rd Semester B.Tech.(Information Technology) programme under Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018 approved.(Annexure VII)

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RESOLVED that the curriculum of B.E Electronics and Communication Engineering under the Regulations 2018 to be followed as per choice based credit systems for the batch of the students to be admitted during 2018-2019 be approved .(Annexure VIII)

19.10 To consider the Syllabi of 3rd Semester M.E (Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018.

RESOLVED that the Syllabi of 3rd Semester M.E (Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018 be approved .(Annexure IX)

19.11 To consider the Curriculum & Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 for the batch of students to be admitted in 2013-2019.

RESOLVED that the Curriculum & Syllabi of 1st Semester M.E. (Communication Systems) programme under the Regulations 2018 for the batch of students to be admitted in 2018-2019 be approved. (Annexure X)

19.12 To consider the Syllabi of 3rd Semester M.E.(VLSI Design) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018.

RESOLVED that the Syllabi of 3rd Semester M.E.(VLSI Design) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018 be approved. (Annexure XI)

19.13 To consider the Curriculum & Syllabi of 1st Semester M.E. (VLSI Design) programme under the Regulations 2018 for the batch of students to be admitted in 2018-2019.

RESOLVED that the Curriculum & Syllabi of 1st Semester M.E. (VLSI Design) programme under the Regulations 2018 for the batch of students to be admitted in 2018-2019 be approved. (Annexure XII)

19.14 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

RESOLVED that Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved. (Annexure XIII)

Date: 07.04.2018

Signature of Chairman - BoS

Chairman, Baard of Courties

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ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Members Present for the Nineteenth Board of Studies Meeting held on 21.04,2018

	Members Present for the Nineteenth	Board of Stades 12		
No.	Name & Address of the Mer	nbers	Category	Signature
1	Dr. S. Sumathi, Ph.D (VLSI)., Prof & Head, Dept. of ECE, ACE, Hosur.	4	BoS-Chairman	معوقول
2	Dr. K.A.Dattatreya, M.E (Electronics), MBA, I Professor, Dept. of ECE, ACE, Hosur.	h.D	Internal Member	alle
3	Dr. T.Menakadevi., M.Tech (VLSI), Ph.D., Professor, Dept. of ECE, ACE, Hosur.	: .	Internal Member	- Myller
4	Mr.M.Sivakumar, M.E (Communication System AP, Dept. of ECE, ACE, Hosur.	ms).,	Internal Member	1.2
5	Mr.S.Chidambaram.,M.E(Power Electronics & AP, Dept. of ECE, ACE, Hosur.	Drives)	Internal Member	-S. UNIO
6	Dr.T.S.Udhaya Suriya ., M.E.,Ph.D., Professor & Head Department of BME, ACE, Hosur.	*	Interdisciplinary Member	Len
7	Dr.E.Saravana kumar., M.E (Computer Science Professor, Dept. of CSE, ACE, Hosur.	e and Engineering),Ph.D	Interdisciplinary Member	By
8	Prof.Dr.L.Ganesan Prof & Head Department of CSE Alagappa Chettiar College of Engineering and Karaikudi – 630 004.	l Technology	University Nominee	21.41
9	Prof.Dr.G.Geetha Associate Professor Department of ECE CEG Campus	10	Academic Council Nominee	G Cee
10	Anna University, Chennai-25 Prof.Dr.K.J. Shanthi Professor & Head Department of Medical Electronics Dr. Ambedkar Institute of Technology	3	Academic Council Nominee	Shand
11	Mallathalli, Bangalore – 56 Shri.J.Ravi Kumar Proprietor Technilab Instrument Bangalore – 560033		Industry Representative	Ve
12	Shri N.Thandayuthapani Senior Software Engineer Robert-Bosch Engineering Business Solutio	ns	Alumnus Member	main
	Bangalore.		\ - Q	and the same

Date: 21.04.2018

Signature of the Chairman-BoS
Chairman, Board of Station
Faculty of Electronics and Communication
Adhiyamaan College of Engineering (Autonomount Honur - 635 109
Krishnagiri (Ut), Tumil Nadu

Sem	Course	Name of the course	Remarks (New course Introduced/contents incorporated in the corresponding
			incorporated in the corresponding course Newly Introduced Course. The unit modules are as follows:
VII	715ECT01	RF System Design	 RF Issues RF Filter Design Active RF Components And
	A.		Applications RF Amplifier Designs Oscillators, mixers & Applications
VII	715ECT02	Optical Communication	The following topics are included in the existing syllabus, • Principles of fiber nonlinearities.
VII	715ECT03	Microwave Engineering	The following topics are included in the existing syllabus, • Microwave T junction: E- Plane Tee, H-Plane Tee, Magic Tee • Tunnel diodes
	715ECT04	Embedded Systems	The following topics are included in the existing syllabus, Distributed Embedded Architecture - Hardware and Software Architectures, Networks for embedded systems- I2C, CAN Bus, SHARC link ports, Ethernet, Internet. Design Example: Elevator Controller. Data Compressor-Alarm clock-Cell phones-Audio player-Software Modem-Digital still camera-Telephone answering machine-Engine control unit.
VII	715ECE01	Engineering Ethics and J Human Values	Newly Introduced course
VII 715ECE03 High Speed Networks		ligh Speed Networks	The following topics are included in the existing syllabus, • ATM and MPLS.
m :		leural Networks and its polications	The following topics are included in the existing syllabus, • Content Addressable Nature — Binary Hopfield Network

VII	715ECE07	Remote Sensing	Newly Introduced course
VII	715ECE08	Wireless Networks	The existing syllabus has been entirely reframed with the following unit modules WIRELESS LAN MOBILE NETWORK LAYER MOBILE TRANSPORT LAYER WIRELESS WIDE AREA NETWORK 4G NETWORKS
VII	715ECE10	Wireless Communication	Newly Introduced Course. The unit modules are as follows: WIRELESS CHANNELS CELLULAR ARCHITECTURE DIGITAL SIGNALING FOR FADING CHANNELS MULTIPATH MITIGATION TECHNIQUES MULTIPLE ANTENNA TECHNIQUES
VII	715ECE11	Advanced Microcontrollers	Newly Introduced Course. The unit modules are as follows: RISC PROCESSOR CISC PROCESSORS MSP4316-BIT MICROCONTROLLER PROGRAMMING AND PERIPHERALS INTERFACE USING MSP430 FAMILIES COMMUNICATION INTERFACE USING MSP430 MICROCONTROLLER
VII	715ECP07	Optical and Microwave Laboratory	The following experiments are included in the existing syllabus, • DC Characteristics of LED and LASER Diode. • Analog/Digital transmission through optical fiber link.

Sem	Course Code	Course Name	Remarks
	20		(New course Introduced/ Contents incorporated in the
			corresponding course)
1	118ESE06	Basic	Newly Introduced Course. The unit modules are as
		Electrical Electronics	follows:
		and	INTRODUCTION TO BASIC ELECTRICAL ELEMENTS
		Instrumentati	FUNDAMENTALS OF DC AND AC CIRCUITS
		on	SEMICONDUCTOR DEVICES AND APPLICATIONS
		Engineering	DIGITAL ELECTRONICS
			MEASUREMENT SYSTEMS AND TRANSDUCERS
VI	618ECE05	Soft	Newly Introduced Course. The unit modules are as
		Computing	follows:
		and	INTRODUCTION TO SOFT COMPUTING AND
		Applications	NEURAL NETWORKS
			GENETIC ALGORITHMS
		- 5	NEURAL NETWORKS
			FUZZY LOGIC NEURO SUZZY MODELING
VI	618ECE06	Speech	NEURO-FUZZY MODELING Newly Introduced Course. The unit modules are as
N I S	DISECTOR	Processing	follows:
= "		Troccssing	
			NATURE OF SPEECH SIGNAL TIME DOMAIN METHODS FOR SPEECH
			TIME DOMAIN METHODS FOR SPEECH PROCESSING
	2.	·	FREQUENCY DOMAIN METHODS FOR SPEECH
			PROCESSING
			 LINEAR PREDICTIVE CODING OF SPEECH
			HOMOMORPHIC SPEECH ANALYSIS
VI	618ECE02	Robotics	Newly Introduced Course. The unit modules are as
		Engineering	follows:
			INTRODUCTION
			ELEMENTS OF ROBOTS-JOINTS, LINKS,
			ACTUATORS AND SENSORS
			 END EFFECTORS AND ROBOT CONTROLS
			ROBOT CELL DESIGN AND APPLICATIONS
			MICRO/NANO ROBOTICS SYSTEM
VII	718ECT01	Adhoc and	Newly Introduced Course. The unit modules are as
		Wireless ?	follows:
		Sensor	AD HOC NETWORKS – INTRODUCTION AND
		Networks	ROUTING PROTOCOLS
	131		SENSOR NETWORKS – INTRODUCTION &
	=	§ 5	ARCHITECTURES

	11-		
			 WSN NETWORKING CONCEPTS AND PROTOCOLS
			SENSOR NETWORK SECURITY
			SENSOR NETWORK PLATFORMS AND TOOLS
·VII	718ECE03	Multimedia	Newly Introduced Course. The unit modules are as
		Compression	follows
		Techniques	MULTIMEDIA COMPONENTS
			TEXT COMPRESSION
	8	E **	AUDIO COMPRESSION
			IMAGE COMPRESSION
			VIDEO COMPRESSION
VII	718ECE04	Nano	Newly Introduced Course. The unit modules are as
V 11	7102004	Technology	follows:
	=		INTRODUCTION
			DIVERSITY IN NANO SYSTEMS
			METAL NANO PARTICLES AND NANO SHELLS
	9 - 3	W.	EVOLVING INTERFACES IN NANO
			SOCIETY AND NANO TECHNOLOGY
VII	718ECE06	Optical	Newly Introduced Course. The unit modules are as
		Networks	follows:
			INTRODUCTION TO OPTICAL NETWORKS AND
			COMPONENTS
			SINGLE AND MULTI-HOP NETWORKS
			OPTICAL SWITCHING
			OPTICAL ACCESS NETWORKS & METRO
			NETWORKS
			ROUTING AND OPTICAL MULTICASTING
VII	718ECE07	Cognitive	Newly Introduced Course. The unit modules are as
		Radio	follows.
			 INTRODUCTION TO SOFTWARE DEFINED RADIO
			THE STATE OF THE SECOND OF THE
			SDR ARCHITECTURE
	a a		SDR ARCHITECTURE
1			 SDR ARCHITECTURE INTRODUCTION TO COGNITIVE RADIOS
VII	718ECE11	Detection and	 SDR ARCHITECTURE INTRODUCTION TO COGNITIVE RADIOS COGNITIVE RADIO ARCHITECTURE
VII	718ECE11	Estimation	 SDR ARCHITECTURE INTRODUCTION TO COGNITIVE RADIOS COGNITIVE RADIO ARCHITECTURE NEXT GENERATION WIRELESS NETWORKS
VII	718ECE11		 SDR ARCHITECTURE INTRODUCTION TO COGNITIVE RADIOS COGNITIVE RADIO ARCHITECTURE NEXT GENERATION WIRELESS NETWORKS Newly Introduced Course. The unit modules are as
VII	718ECE11	Estimation	 SDR ARCHITECTURE INTRODUCTION TO COGNITIVE RADIOS COGNITIVE RADIO ARCHITECTURE NEXT GENERATION WIRELESS NETWORKS Newly Introduced Course. The unit modules are as follows:
VII	718ECE11	Estimation	SDR ARCHITECTURE INTRODUCTION TO COGNITIVE RADIOS COGNITIVE RADIO ARCHITECTURE NEXT GENERATION WIRELESS NETWORKS Newly Introduced Course. The unit modules are as follows: HYPOTHESIS TESTING
VII	718ECE11	Estimation	 SDR ARCHITECTURE INTRODUCTION TO COGNITIVE RADIOS COGNITIVE RADIO ARCHITECTURE NEXT GENERATION WIRELESS NETWORKS Newly Introduced Course. The unit modules are as follows: HYPOTHESIS TESTING SIGNAL DETECTION APPLICATIONS

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VIII	818ECE11	Advanced	Newly Introduced Course. The unit modules are as
		Wireless	follows:
	,	Communicati	CAPACITY OF WIRELESS CHANNELS
	/.	on	RADIO WAVE PROPAGATION
			SPACE TIME BLOCK CODES
		*	SPACE TIME TRELLIS CODES
			LAYERED SPACE TIME CODES
VIII	818ECE12	DSP	Newly Introduced Course. The unit modules are as
		Architecture	follows:
	1 2	and	 FUNDAMENTALS OF PROGRAMMABLE DSPS
	T _C	Programming	TMS320C3X PROCESSOR
			ADSP PROCESSORS
			ADVANCED PROCESSORS I
		21	ADVANCED PROCESSORS II

Value-Added Courses are recommended.

		SYSTEMS

		50) 	
Sem	Course Códe	Course Name	Remarks (New course Introduced/ Contents incorporated in the corresponding course)
1	118COE04	Wavelet Signal Processing	Newly Introduced Course. The Unit Modules are as follows: FUNDAMENTALS MULTI RESOLUTION ANALYSIS CONTINUOUS WAVELET TRANSFORMS DISCRETE WAVELET TRANSFORM APPLICATIONS
1	118COE05	WDM Optical Networks	Newly Introduced Course. The Unit Modules are as follows: OPTICAL SYSTEM COMPONENTS OPTICAL NETWORK ARCHITECTURES WAVELENGTH ROUTING NETWORKS PACKET SWITCHING AND ACCESS NETWORKS NETWORK DESIGN AND MANAGEMENT
II	218COE05	Analysis and Design of Planar Transmission Lines	Newly Introduced Course. The Unit Modules are as follows: ANALYSIS OF PLANAR TRANSMISSION LINES SPECTRAL DOMAIN METHODS ANALYSIS OF HYBRID MODE DESIGN OF COPLANAR & SLOT LINES MICROSTRIP LINES

, II	218COE14	DSP Processor Architecture and Programming	FUNDAMENTALS OF PROGRAMMABLE DSPS TMS320C3X PROCESSOR ADSP PROCESSORS ADVANCED PROCESSORS I ADVANCED PROCESSORS II
H.	218COE16	Smart Antennas	INTRODUCTION TO SMART ANTENNAS SMART ANTENNAS TECHNIQUES FOR CDMA CHANNEL MODELS CAPACITY IMPROVEMENT OF CDMA ESTIMATION TECHNIQUES
11	218COE17	Cognitive Radio	Newly Introduced Course. The Unit Modules are as follows: INTRODUCTION TO SOFTWARE DEFINED RADIO SDR ARCHITECTURE INTRODUCTION TO COGNITIVE RADIOS COGNITIVE RADIO ARCHITECTURE NEXT GENERATION WIRELESS NETWORKS
11	218COE18	Ultra Wide Band Communication	 Newly Introduced Course. The Unit Modules are as follows: UWB SIGNALS AND SYSTEMS WITH UWB WAVEFORMS SIGNAL PROCESSING TECHNIQUES FOR UWB SYSTEMS AND UWB CHANNEL MODELING UWB COMMUNICATIONS AND ADVANCED UWB PULSE GENERATION UWB ANTENNAS & ARRAYS, POSITION & LOCATION WITH UWB SIGNALS UWB COMMUNICATION STANDARDS AND ADVANCED TOPICS IN UWB COMMUNICATION SYSTEMS
111	318COP02	Internship	Newly Introduced Course
111	318COE05	OFDM for Communication Systems	Newly Introduced Course: The Unit Modules are as follows: INTRODUCTION TO OFDM SYSTEM MODELING CONCEPT OF MULTICARRIER TRANSMISSION SYNCHRONIZATION PAPR AND HYBRID CONCEPTS APPLICATION OF OFDM

			Newly Introduced Course. The Unit Modules are as follows:
1		Danmfarmina in	INTRODUCTION TO BEAMFORMING
		Beamforming in	ADAPTIVE BEAMFORMING
III	318COE06	Wireless	SUBBAND ADAPTIVE BEAMFORMING
		Communication	DIGITAL BEAMFORMING
		W.	ERROR EFFECTS IN DBF
1			Newly Introduced Course. The Unit Modules are as follows:
		Advanced	PERSONAL COMMUNICATION SYSTEMS
	24000542	Advanced	WIRELESS LOCAL AREA NETWORKS
Ш	318COE12	Mobile	MOBILE INTERNET PROTOCOL
		Computing	WIRELESS APPLICATION PROTOCOL
			WIRELESS LOCAL LOOP TECHNIQUES
_	·		
			Newly Introduced Course. The Unit Modules are as follows:
		Advanced	INTRODUCTION TO WIRELESS RECEPTION
		Techniques for	4 Jr
ш	318COE14	Wireless	DETECTION AND PERFROMANCE TRACE AND CRACE TRACE AN
		Reception	TDMA AND CDMA
		neception	ADAPTIVE TECHNIQUES
		X	MIMO CHANNELS
			Newly Introduced Course. The Unit Modules are as follows:
	, ,	Wavelets and	• FUNDAMENTALS
111	318COE15	Mutiresolution	MULTI RESOLUTION ANALYSIS
		Processing.	CONTINUOUS WAVELET TRANSFORMS
· 1		.,	DISCRETE WAVELET TRANSFORM
			• APPLICATIONS
			M.E – VLSI DESIGN
	2	r	Remarks
Sem	Course Code	Course Name	(New course Introduced/ Contents incorporated in the
Jeni	Course couc	Course Warne	(New course introduced) contents incorporated in the
	×	w	corresponding course)
	18.		Newly Introduced Course. The unit modules are as
			follows:
		*	WITHOUT ON AND CHARACTERISTICS OF AS IS
	1		INTRODUCTION AND CHARACTERISTICS OF AD/DA
ı	118VLE01	Data Converters	CONVERTER
11011101		6	SWITCH CAPACITOR CIRCUITS AND COMPARATORS
			NYQUIST RATE D/A CONVERTERS
		:	PIPELINE AND OTHER ADCs
	*		SIGMA DELTA CONVERTERS

		in the second se	
1	118VLE04	Security Solutions in VLSI	Newly Introduced Course. The unit modules are as follows: BASIC CONCEPTS ENCRYPTION TECHNIQUES FIREWALLS AND CYBER LAWS FUTURE THREATS TO NETWORK CRYPTO CHIP DESIGN
11	218VLE03	Solid State Device Modelling and Simulation	Newly Introduced Course. The unit modules are as follows: MOSFET DEVICE PHYSICS DEVICE MODELLING MULTISTEP METHODS MATHEMATICAL TECHNIQUES FOR DEVICE SIMULATIONS SIMULATION OF DEVICES
.11,	218VLE06	Artificial Intelligence and Optimization Techniques	Newly Introduced Course. The unit modules are as follows: • NEURAL NETWORKS • FUZZY LOGIC SYSTEMS • EVOLUTIONARY COMPUTATION AND GENETIC ALGORITHMS • ANT COLONY OPTIMIZATION • PARTICLE SWARM OPTIMIZATION
11	218VLE11	Introduction to MEMS System Design	Newly Introduced Course. The unit modules are as follows: INTRODUCTION TO MEMS MECHANICS FOR MEMS DESIGN ELECTRO STATIC DESIGN CIRCUIT AND SYSTEM ISSUES INTRODUCTION TO OPTICAL AND RF MEMS
II Å	218VLE15	Scripting languages	Newly Introduced Course. The unit modules are as follows: INTRODUCTION TO SCRIPTS AND SCRIPTING ADVANCED PERL TCL ADVANCED TCL

		T	
	W ×	2	TK AND JAVASCRIPT
			Newly Introduced Course. The unit modules are as follows:
11	218VLE18	Advanced MOSFET Modelling	 BASIC DEVICE PHYSICS MOSFET DEVICES NANO-SCALED CLASSICAL MOSFETS NON-CLASSICAL MOSFETS COMPACT MODELS FOR CIRCUIT SIMULATORS
		2 2 2 2 2 1	Newly introduced Course. The unit modules are as follows:
111	318VLE04	Nano Scale Transistors	 INTRODUCTION TO NOVEL MOSFETS PHYSICS OF MULTIGATE MOS SYSTEM NANOWIRE FETS AND TRANSISTORS AT THE MOLECULAR SCALE RADIATION EFFECTS CIRCUIT DESIGN USING MULTIGATE DEVICES
111	318VLE08	Genetic Algorithms and its Applications	Newly Introduced Course. The unit modules are as follows: INTRODUCTION TO GENETIC ALGORITHMS PARTITIONING PLACEMENT AND ROUTING GENETIC ALGORITHMS IN VLSI TESTING FPGA TECHNOLOGY MAPPING AND PEAK POWER ESTIMATION
III	318VLE09	Submicron VLSI Design	Newly Introduced Course. The unit modules are as follows: SILICON REALIZATION OF ASIC LOW POWER DESIGN DESIGN FOR RELIABILITY DEEP SUB MICRON CMOS DEVICES
III	318VLE10	IP Based VLSI Design	Newly Introduced Course. The unit modules are as follows: • VLSI AND ITS FABRICATION • COMBINATIONAL LOGIC NETWORKS

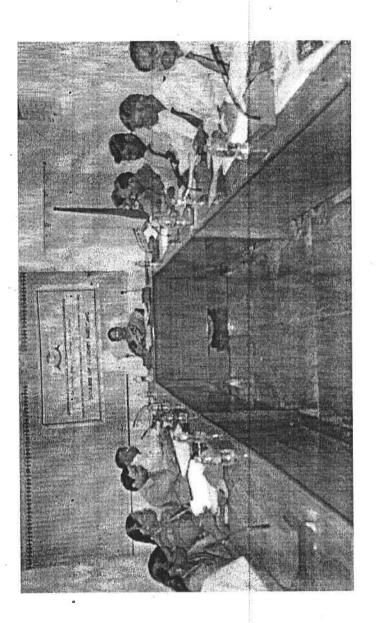
	27 27		 SUBSYSTEM DESIGN FLOOR PLANNING AND ARCHITECTURE DESIGN DESIGN SECURITY
III 31 8	BVLE18	High Speed VLSI	Newly Introduced Course. The unit modules are as follows: RECONFIGURABLE COMPUTING HARDWARE MAPPING DESIGNS INTO RECONFIGURABLE PLATFORMS COMPUTATIONAL ARCHITECTURES FOR FP OPTICAL RECONFIGURATION MODELS MULTI CORE ARCHITECTURES

Feedback obtained about curriculum and syllabus from stakeholders (Students, Faculty, Employers & Alumni) were also discussed and concluded.

BOS CHAIRMAN

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & FC)
Adhiyamaan College of Engineering (Autonomous)
Hosur - 635 109
Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING [AUTONOMOUS], HOSUR-635109 FACULTY OF ELECTRONICS & COMMUNICATION ENGINEERING[UG&PG] PHOTO OF 19th BoS MEETING ON 21.04.2018



Clockwise:

Dr.T.S.Udhaya Suriya, Dr.T.Menakadevi, Shri.J.Ravikumar, Dr.L.Ganesan, Dr.S.Sumathi, Dr.G.Geetha, Dr.K.A.Dattatreya, Dr.E.Saravanakumar, Prof.M.Sivakumar, Prof.S.Chidambaram.



ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109

(Autonomous)

BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING Minutes of the Eighteenth Meeting

Minutes of the 18th Meeting of the Board of Studies in Electronics & Communication Engineering held on 28.10.2017 at 10.00 A.M.

MEMBERS PRESENT

- 1 Dr. S. Sumathi, Chairman
- 2. Dr. K.A.Dattatreya, Internal Member
- 3. Dr. T Menakadevi, Internal Member
- 4. Dr.S.Sathish,Internal Member
- 5. Prof.M.Sivakumar, Internal Member
- 6. Prof.S.Chidambaram, Internal Member
- 7. Dr.T.S.Udhaya Suriya, Interdisciplinary Member, HoD/BME
- 8. Dr.L.Ganesan ,University Nominee
- 9. Dr.G.Geetha, Academic Council Nominee
- 10. Dr.K.J.Shanthi, Academic Council Nominee
- 11. Shri.J.Ravi Kumar ,Industry Representative
- 12. Shri.N.Thandayuthapani, Alumnus Member
- 18.1 To consider the minutes of the 17th Board of Studies meeting in Electronics and Communication Engineering held on 01.04.2017.
 - **RESOLVED** that the minutes of the 17th Board of Studies meeting in Electronics and Communication Engineering held on 01.04.2017 have been considered.
- 18.2 To consider the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2011 for the batch of students admitted in 2014 2015.
 - **RESOLVED** that the Syllabi of 8th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2011 for the batch of students admitted in 2014 2015 be approved (Annexure I)
- 18.3 To consider the Syllabi of 6th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2015 2016.

- **RESOLVED** that the Syllabi of 6th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2015 2016 be approved (Annexure II)
- 18.4 To consider the Syllabi of 4th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 2017.
 - **RESOLVED** that the Syllabi of 4th Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 2017 be approved (Annexure III)
- 18.5 To consider the Syllabi of "Microprocessors and Microcontrollers" and "Signals and Systems" for 4th Semester B Tech (Information Technology) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 2017.
 - **RESOLVED** that the Syllabi of "Microprocessors and Microcontrollers" and "Signals and Systems" for 4th Semester B.Tech (Information Technology) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 2017 be approved (Annexure IV)
- 18.6 To consider the Syllabi of "Electric Circuits and Electron Devices" and "Circuits and Devices Laboratory" for 2nd Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2015(CBCS) for the batch of students admitted in 2017 2018.
 - **RESOLVED** that the Syllabi of "Electric Circuits and Electron Devices" and "Circuit and Devices Laboratory" for 2nd Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2015(CBCS) for the batch of students admitted in 2017 2018 be approved (Annexure V)
- 18.7 To consider the Syllabi of 2nd Semester M.E. (Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018.
 - **RESOLVED** that the Syllabi of 2nd Semester M.E. (Communication Systems) programm under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018 by approved (Annexure VI)
- 18.8 To consider the Syllabi of 4th Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016-2017.
 - **RESOLVED** that the Syllabi of 4th Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016-2017 be approved (Annexure VII)

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18.9 To consider the Syllabi of 2nd Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018.

RESOLVED that the Syllabi of 2nd Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018 be approved (Annexure VIII)

18.10 To consider the Syllabus of "Digital Image Processing" for 2nd Semester M.E. (Computer Science and Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018.

RESOLVED that the Syllabus of "Digital Image Processing" for 2nd Semester M.E. (Computer Science and Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018 be approved. (Annexure IX)

18.11 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

RESOLVED that the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved (Annexure X)

Date: 28.10.2017

Signature of Chairman -BoS

Chairman, Board of Studies

Faculty of Electronics and Communication Engineering (UG & PG)

Adhiyamaan College of Engineering (Autonomous)

Hosur - 635 109

Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HUSUK-033 107 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Members Present for the Eighteenth Board of Studies Meeting held on 28.10.2017

	Members Present for the Eighteenth Board of		Category	Signature	
l.No.	Name & Address of the Member			0.5.	
1	Dr. S. Sumathi, Ph.D (VLSI)., Prof & Head, Dept. of ECE, ACE, Hosur.		BoS-Chairman	Jueses	
2	Dr. K.A.Dattatreya, M.E (Electronics), MBA Professor, Dept. of ECE, ACE, Hosur.	A, Ph.D	Internal Member	alth	
3	Dr. T.Menakadevi., M.Tech (VLSI), Ph.D., Professor, Dept. of ECE, ACE, Hosur.		Internal Member	- letie	
4	Dr.S.Sathish.,M.E (Communication System Associate Professor, Dept. of ECE, ACE, H.	0.00.20	Internal Member	8.8mm	
5	Mr.M.Sivakumar, M.E(Communication Sys	tems).,	Internal Member	3	
6	Mr.S.Chidambaram., M.E(Power Electronic AP, Dept. of ECE, ACE, Hosur.	S & Drives)	Internal Member Interdisciplinary	- rest	
7	Dr.T.S.Udhaya Suriya Professor & Head Department of BME, ACE, Hosur.		Member	gudhey - 5	
8	Prof.Dr.L.Ganesan Prof & Head Department of CSE Alagappa Chettiar College of Engineering Technology, Karaikudi – 630 004.	and	University Nominee	12 12 13.	
	Prof.Dr.G.Geetha Associate Professor Department of ECE CEG Campus Anna University, Chennai-25.	_ *	Academic Council Nominee	G. Creen	
	Prof.Dr.K.J.Shanthi Professor & Head Department of Medical Electronics Dr. Ambedkar Institute of Technology Mallathalli, Bangalore – 56.	-18- -18-	Academic Council Nominee	Shanding	
	Shri.J.Ravi Kumar Proprietor Technilab Instrument Bangalore – 560033.		Industry Representativ	•	
	Shri N.Thandayuthapani Senior Software Engineer Robert-Bosch Engineering Business So	lutions	Alumnus Member	wann	
	Bangalore.			008	

Date: 28.10.2017

Signature of the Chairman-BoS

Chairman, Board of Studies
Faculty of Electronics and Communication Engineering (UG & PG)
Adhiyamaan College of Engineering (Autonomous)
Hosur - 635 109
Krishnagiri (Ot), Tamil Nadu.

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Department of ECE- 18th BOS Recommendations Remarks Sem Course Code Name of the course (New course Introduced/contents incorporated in the corresponding course) The following topics are included in the existing syllabus. Calculation of voltage, current and power delivered and efficiency of transmission Voltage and current on the Transmission Lines and dissipation-less line, Open and VI 615ECT01 Waveguides short circuited lines. Impedance matching by stubs Characteristic Impedance of symmetrical networks. Constant K – low pass, high pass, band pass, band elimination The following topics are included in the existing syllabus. VΙ 615ECT02 VLSI Design AOI and OAI logic. VHDL Description of Combinational Networks The following topics are included in the existing syllabus, Digital Multiplexers. VI 615ECT03 Digital Communication QAM-DPSK. Matched filters. The following topics are included in the existing syllabus, Polarization mismatch, Antenna noise temperature Radiation from rectangular apertures, Uniform and Tapered aperture, Numerical tool for Antenna and Wave VI 615ECT04 antenna analysis. Propagation Basic principle of antenna Synthesis-Binomial array Principle of frequency independent antennas - Spiral antenna, Modern antennas Troposcatter propagation, Multi hop propagation The following experiments are included in 615ECP08 VLSI Design Laboratory VΙ the existing syllabus, 8-bit Subtractor The following topics are included in the existing syllabus, Filter bank realization of the Analog and Digital periodogram 615ECP09 Communication VI Parametric methods - ARMA, AR and MA model based spectral Systems Laboratory estimation, Solution using Levinson-Durbin algorithm

VI	615ECE01	Digital Image Processing	The following topics are included in the existing syllabus, • Hough Transform
VI	615ECE02	Advanced Digital Signal Processing	The following topics are included in the existing syllabus, • Filter bank realization of the periodogram • Parametric methods – ARMA, AR and MA model based spectral estimation, • Solution using Levinson-Durbin algorithm
VI	615ECE03	Digital System Design using VHDL	The following topics are included in the existing syllabus, • Memory and Register
VΙ	615ECE04	Information Theory Coding	The existing syllabus has been entirely reframed with the following unit modules

Feedback obtained about curriculum and syllabus from stakeholders(Students, Faculty, Employers & Alumni) were also discussed and concluded.

BOS CHAIRMAN

Chairman, Board of Studies Faculty of Electronics and Companication Engineering (UG & FC)
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Dr.S.Sumathi, Dr.G.Geetha, Shri.J.Ravikumar, Dr.L.Ganesan, Dr.T.Menakadevi, Dr.T.S.Udhaya Suriya, Prof.M.Sivakumar, Dr.K.A.Dattatreya, Prof.S.Chidambaram. Ör.S.Sathish, Clockwise:

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