



**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS),**  
**HOSUR-635 130**  
**BOARD OF STUDIES IN FACULTY OF ELECTRONICS & COMMUNICATION**  
**ENGINEERING**

**33<sup>rd</sup> Meeting on 20.12.2025**

**AGENDA**

- 33.1 To consider the Minutes of the 32<sup>nd</sup> Board of Studies meeting in Electronics and Communication Engineering held on 05.07.2025.
- 33.2 To consider about the 8<sup>th</sup> Semester curriculum and syllabus under the Regulations-2022(CBCS) for the batch of students admitted in 2022-2027.
- 33.3 To consider about the 6<sup>th</sup>, 4<sup>th</sup>, 2<sup>nd</sup> Semester curriculum and syllabus under the Regulations-2022 (CBCS).
- 33.4 To consider the curriculum and Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2022 (CBCS) for the batch of students admitted in 2023-2024.
- 33.5 To discuss and consider about the 2<sup>nd</sup> semester syllabus under the Regulations-2025 (CBCS) for M.E- Communication Systems (AY 2025-26) for the batch of students admitted in 2025-26.
- 33.6 To discuss and consider about the 2<sup>nd</sup> semester syllabus under the Regulations-2025 (CBCS) for B.E- Electronics and Communication Engineering for the batch of students admitted in 2025-26.
- 33.7 To discuss and consider about the 2<sup>nd</sup> semester syllabus for the course "Digital Principles and Computer Organization" under the Regulations-2025 (CBCS) for B.E- Computer Science and Engineering programmes for the batch of students admitted in 2025-26.
- 33.8 To consider the Syllabus of "Digital Electronics" for 4<sup>th</sup> Semester B.E. (Computer Science and Engineering-cybersecurity) programme under the Regulation 2022 (CBCS) for the batch of students admitted in 2024 - 2025.
- 33.9 To consider the CDIO approach under the Regulations-2022 and Regulations- 2025.
- 33.10 To consider the NPTEL/SWAYAM Courses for the batch Students 2023-2027 and 2024-2028 for credit transfer.



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- 33.11 To consider the Naan Mudhalvan courses under Regulations-22 for the batch 2023-27 and 2024-2028.
- 33.12 To Consider the QNX course for the students' batch 2023-27 under the Regulations- 2022.
- 33.13 To consider and discuss the Feedback obtained about curriculum and syllabus from Stakeholders (Students, Faculty, Employers & Alumni).
- 33.14 To consider the Value-Added Course/ Skill-based Add-on/Certificate Courses in B.E. (Electronics and Communication Engineering) programme under the Regulations 2022 (CBCS).
- 33.15 Any Other.

**Date: 20.12.2025**

*Jeevan*  
20/12/2025  
**BoS CHAIRPERSON**

Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UG & FC)  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.



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**BOARD OF STUDIES IN FACULTY OF ELECTRONICS & COMMUNICATION  
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**Minutes of the 33<sup>rd</sup> Meeting**

Minutes of the 33<sup>rd</sup> Meeting of the Board of Studies in Electronics & Communication Engineering 20.12.2025 held on at 10.30 A.M.

**MEMBERS PRESENT**

1. Dr. S. Sumathi, **BoS Chairperson**
2. Dr. G. Kavithaa, **Anna University Nominee**
3. Dr. C. Paramasivam, **Subject expert from outside the parent university, nominated by Academic Council**
4. Dr. S. Thenmozhi, **Subject expert from outside the parent university, nominated by Academic Council**
5. Mr. David Kingsley M P, **Industry Representative**
6. Mrs. Bharani S, **Industry Representative**
7. Mr. Ramachandran Krishnaswamy, **Alumni Representative**
8. Dr.T. Menakadevi, **Internal Member**
9. Dr. S. Xavier Arockiaraj, **Internal Member**
10. Dr.M.Ashok Kumar, **Internal Member**
11. Mr.P.Manivannan, **Internal Member**
12. Mr.M.Venkatesan, **Internal Member**
13. Other staff members of the same faculty.

**33.1 To consider the Minutes of the 32<sup>nd</sup> Board of Studies meeting in Electronics and Communication Engineering held on 05.07.2025.**

**Resolved** that the Minutes of the 32<sup>nd</sup> Board of Studies meeting in Electronics and Communication Engineering held on 05.07.2025.

**33.2 To consider about the 8<sup>th</sup> Semester curriculum and syllabus under the Regulations-2022 (CBCS) for the batch of students admitted in 2022-2027.**

**RESOLVED** that the 8<sup>th</sup> Semester curriculum and syllabus under the Regulations-2022 (CBCS) for the batch of students admitted in 2022-2027. (Annexure-1)

**33.3 To consider about the 6<sup>th</sup>, 4<sup>th</sup>, 2<sup>nd</sup> Semester curriculum and syllabus under the Regulations-2022 (CBCS).**

**RESOLVED** that the 6<sup>th</sup>, 4<sup>th</sup>, 2<sup>nd</sup> Semester curriculum and syllabus under the Regulations-2022 (CBCS) (Annexure-2).

**33.4 To consider the curriculum and Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2022 (CBCS) for the batch of students admitted in 2023-2024.**





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**RESOLVED** that the curriculum and Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2022 (CBCS) for the batch of students admitted in 2023-2024 (Annexure-3).

- 33.5 To discuss and consider about the 2<sup>nd</sup> semester syllabus under the Regulations-2025 (CBCS) for M.E- Communication Systems for the batch of students admitted in 2025-26. RESOLVED and CONSIDERED** that the 2<sup>nd</sup> semester syllabus under the Regulations-2025 (CBCS) for M.E- Communication Systems for the batch of students admitted in 2025-26 (Annexure-4).
- 33.6 To discuss and consider about the 2<sup>nd</sup> semester syllabus under the Regulations-2025 (CBCS) for B.E- Electronics and Communication Engineering for the batch of students admitted in 2025-26. RESOLVED and CONSIDERED** that the 2<sup>nd</sup> semester syllabus under the Regulations-2025 (CBCS) for B.E- Electronics and Communication Engineering for the batch of students admitted in 2025-26 (Annexure-5).
- 33.7 To discuss and consider about the 2<sup>nd</sup> semester syllabus for the course "Digital Principles and Computer Organization" under the Regulations-2025 (CBCS) for B.E- Computer Science and Engineering programmes for the batch of students admitted in 2025-26. RESOLVED and CONSIDERED** that the 2<sup>nd</sup> semester syllabus for the course "Digital Principles and Computer Organization" under the Regulations-2025 (CBCS) for B.E- Computer Science and Engineering programmes for the batch of students admitted in 2025-26 (Annexure-6).
- 33.8 To consider the Syllabus of "Digital Electronics" for 4<sup>th</sup> Semester B.E. (Computer Science and Engineering-cybersecurity) programme under the Regulation 2022 (CBCS) for the batch of students admitted in 2024 – 2025. RESOLVED and CONSIDERED** that the Syllabus of "Digital Electronics" for 4<sup>th</sup> Semester B.E. (Computer Science and Engineering-cybersecurity) programme under the Regulation 2022 (CBCS) for the batch of students admitted in 2024 – 2025 (Annexure-7).
- 33.9 To consider the CDIO approach under the Regulations-2022 and Regulations- 2025. DISCUSSED and APPROVED** the CDIO approach under the Regulations-2022 and Regulations-2025.
- 33.10 To consider the NPTEL/SWAYAM Courses for the batch Students 2023-2027 and 2024-2028 for credit transfer.**  
Considered the SWAYAM/NPTEL Courses for credit transfer upto 6 credits.  
Approved list of courses
- Introduction to cybersecurity
  - Introduction to Industry 4.0 and Industrial Internet of Things
  - Cyber Security and Privacy
  - Introduction to Machine Learning
  - Biomedical Instrumentation & Sensors





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- Cloud Computing
- Digital Marketing

**33.11 To consider the Naan Mudhalvan courses under Regulations-22 for the batch 2023-27 and 2024-2028.**

Considered the Naan Mudhalvan courses under Regulations-22 for the batch 2023-27 and 2024-2028.

Approved list of courses

- Cybersecurity
- Machine Learning with application to object recognition
- Block chain development
- Digital Marketing
- Cloud Computing
- Full Stack Development

**33.12 To Consider the QNX course for the students' batch 2023-27 under the Regulations-2022**

**RESOLVED and CONSIDERED** that the Syllabus of QNX course for the students' batch 2023-27 under the Regulations- 2022 (Annexure-10).

**33.13 To consider and discuss the Feedback obtained about curriculum and syllabus from Stakeholders (Students, Faculty, Employers & Alumni).**

S. No.	Suggestion given	Sugessted by
1	Integrate AI in core and elective courses	Mr. R. Pradeep, Technology Analyst, Infosys, Bangalore
2	Elective Course Expansion with vide options	Mr. V.Sampath, Founder, Bharath semiconductor society, Chennai
3	Emerging Technology Courses	Mr. Mohanakumar Perumal, Program Analyst(Artificial Intelligence and Analytics), CTS, Coimbatore
4	Skill Development Courses	Kumaradhas S, Senior Engineer, Ericsson Global India Services pvt ltd, Gurgaon
5	Integrate core courses for practical learning	Mr. G. Dineshkumar, Project Lead, Bajaj Electronics, Telangana
6	Data communication and network security course	Mr. Ramachandran Krishnaswamy, Senior Technical Lead, Cisco systems India Pvt Ltd, Bengaluru
7	6G, Quantum Computing	Mr.Thierentharan, Software engineer, Terex India Pvt Ltd, Sipcot 2, Hosur
8	Industry Oriented courses/Summer Internships	Mrs.Renukadevi Selvakumar, Director - HR and marketing, Envimak water Technologies.
9	Focus on skill-based courses, industry standards, and entrepreneurship.	Mr. Ramesh, Dy.Manager, Kia India private limited.
10	Entrepreneurship development for ECE Engineers	Mr. Mohamed Kasim, Senior Manager, DataFlow

**RESOLVED** and Considered feedback from stakeholders was presented.



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- 33.14 To consider the Value-Added Course/ Skill-based Add-on/Certificate Courses in B.E. (Electronics and Communication Engineering) programme under the Regulations 2022 (CBCS).**

**CONSIDERED** the Value-Added Course/ Skill-based Add-on/Certificate Courses in B.E. (Electronics and Communication Engineering) programme under the Regulations 2022 (CBCS).

- ❖ Courses offered in partnership with **industry and NSDC**:
  - i. AI and ML in Robotic and Automation Systems
  - ii. Network Routing and Design
  - iii. 5G Technology in Cellular Mobile Communication
  - iv. LoRa Gateway Design for Real-Time Applications
  - v. ASIC design flow in VLSI Design
  - vi. Controller Area Network and Security in Automotive Industry
  - vii. Embedded Processing with Spartan FPGAs
- ❖ Certificate courses mapped to job roles  
Offered from Semester III onwards (minimum 30 hours per course)

- 33.15 To consider the resolutions to be Passed.**

- Approval of Syllabus changes for UG and PG under Regulations-2025

**Any Other**

The Board Members expressed their sincere appreciation to the Department of Electronics and Communication Engineering for its exemplary efforts in designing and implementing a dynamic, future-ready, and NEP 2020-compliant curriculum. The Board particularly appreciated the introduction of AI-based verticals, integration of Indian Knowledge Systems (IKS), adoption of the CDIO approach, structured internship framework, NSQF-NSDC job role mapping, and the emphasis on research practices, industry collaboration, and outcome-based education.

The suggestions and feedback offered by the Board Members regarding syllabus rationalization, inclusion of emerging technologies such as Generative AI and Agentic AI, strengthening of foundational courses, and enhancement of employability-oriented components were noted and resolved to be incorporated appropriately under Regulations-2025.

**Feedbacks and Action taken:**

<b>Feedback / Suggestion</b>	<b>Action / Resolution Taken</b>
In the 2nd Semester <i>Electric Circuits and Electronic Devices</i> course: • Include two modules on Circuit Theory and Network Analysis • Remove Module V – Special Devices	Resolved to revise the syllabus by strengthening Circuit Theory and Network Analysis content and removing the Special Devices module, in line with foundational requirements and curriculum optimization.





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Reduce the syllabus load of <i>Embedded Systems for Communication with AI</i>	Resolved to rationalize the syllabus by removing overlapping content and optimizing depth to ensure effective learning outcomes.
General suggestion to include Generative AI and Agentic AI topics	Resolved to introduce Generative AI and Agentic AI concepts across relevant professional electives, value-added courses, and interdisciplinary modules under Regulations-2025.
Include Basic VLSI Concepts in <i>Analog and Mixed Signal VLSI Design</i> course	Resolved to incorporate foundational VLSI concepts to strengthen conceptual continuity and industry relevance.

**Concluding Remarks:**

The Chairperson thanked all the Board Members for their valuable time, constructive deliberations, and insightful recommendations. The Board appreciated the Department of Electronics and Communication Engineering for its systematic presentation, academic rigor, and forward-looking curriculum design aligned with **NEP 2020**, **NBA**, and **AICTE** guidelines.

The Chairperson noted that the resolutions passed during the meeting will significantly enhance the academic quality, industry relevance, employability, and research orientation of the UG and PG programmes. The Department was encouraged to continue its efforts in curriculum innovation, outcome-based education, industry collaboration, and integration of emerging technologies such as AI, IoT, VLSI, and next-generation communication systems.

Date: 20.12.2025

**BoS CHAIRPERSON**

Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UG & PG)  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.

**Department of Electronics and Communication Engineering**  
**33<sup>rd</sup> BoS Meeting Held on 20.12.2025**  
**UG Programme Curriculum (CBCS)**  
**Regulations-2025**

BoS Recommendations suggested by Board Members			
Sem	Course Code	Course Name	Remarks (New course Introduced/ Contents incorporated in the corresponding course)
II	225PIT03	Physics for Electronics	Newly Introduced Course
	225PPI05	Python Programming	Integrated Course
	225ECI06	Electric Circuits and Electron Devices	<p><b>UNIT I – BASIC LAWS &amp; DC NETWORK ANALYSIS</b>  <b>Newly Introduced in R-2025</b></p> <ul style="list-style-type: none"> <li>• Basic electrical quantities: voltage, current, power, energy</li> <li>• Network terminology: node, branch, loop, mesh, junction</li> <li>• Independent and dependent sources</li> <li>• DC and AC circuits – concept and differences</li> <li>• Voltage division and current division rules</li> <li>• Source transformation</li> <li>• Design activity: DC distribution network for multiple loads (LEDs, fans, electronic devices)</li> </ul> <p><b>Removed from R-2025</b></p> <ul style="list-style-type: none"> <li>• Two-port network analysis</li> <li>• Duality theorem</li> <li>• Emphasis on source interconnection techniques (series/parallel of independent sources)</li> </ul> <p><b>UNIT II – THEOREMS, TRANSIENTS &amp; AC ANALYSIS</b>  <b>Newly Included Topics</b></p> <ul style="list-style-type: none"> <li>• Reciprocity theorem – statement and applications</li> <li>• Natural and forced response of RL and RC circuits</li> <li>• Explicit AC fundamentals: <ul style="list-style-type: none"> <li>◦ Sinusoidal waveforms</li> <li>◦ Average and RMS values</li> <li>◦ Phasor representation</li> <li>◦ Impedance and phase relationships</li> </ul> </li> <li>• Equivalent circuit modeling of household appliances</li> <li>• Design-oriented activity</li> </ul> <p><b>Removed Topics</b></p> <ul style="list-style-type: none"> <li>• Pulse response analysis</li> <li>• Single tuned and double tuned circuits</li> <li>• Detailed frequency response plotting emphasis</li> </ul> <p><b>UNIT III – SEMICONDUCTOR PHYSICS &amp; DIODES</b>  <b>Newly Included Topics</b></p> <ul style="list-style-type: none"> <li>• Drift and diffusion currents</li> <li>• Mobility</li> <li>• Generation and recombination</li> <li>• Carrier lifetime</li> <li>• Energy band diagram under equilibrium</li> <li>• Rectifier applications</li> <li>• Sensor signal conditioning using diodes (design activity)</li> </ul>



			<p><b>Removed Topics</b></p> <ul style="list-style-type: none"> <li>• Diode current equation</li> <li>• Space charge capacitance</li> <li>• Diffusion capacitance</li> <li>• Detailed temperature effects on diode behavior</li> <li>• Detailed breakdown mechanisms (avalanche, Zener physics)</li> </ul> <p><b>UNIT IV – TRANSISTORS &amp; OPTOELECTRONICS</b></p> <p><b>Newly Included Topics</b></p> <ul style="list-style-type: none"> <li>• Early effect</li> <li>• Regions of operation</li> <li>• Zener diode as voltage regulator</li> <li>• SCR, TRIAC, DIAC – applications oriented</li> <li>• Optocoupler</li> <li>• Automatic lighting control circuit design</li> <li>• Integrated control &amp; automation focus</li> </ul> <p><b>Removed Topics</b></p> <ul style="list-style-type: none"> <li>• Breakdown mechanisms in BJTs</li> <li>• Thermal effects on MOSFET</li> <li>• Detailed drain current equations</li> <li>• Extensive comparison theory between BJT and MOSFET</li> </ul> <p><b>UNIT V – FOUNDATIONS OF ELECTRICAL &amp; TRANSIENT CIRCUITS</b></p> <p><b>Newly Included Topics</b></p> <ul style="list-style-type: none"> <li>• Inductors and capacitors – construction &amp; energy storage</li> <li>• Self and mutual inductance</li> <li>• Magnetic coupling and dot convention</li> <li>• Resonant frequency, bandwidth</li> <li>• Quality factor (Q) – application oriented</li> <li>• Transformers: <ul style="list-style-type: none"> <li>○ Construction</li> <li>○ Turns ratio</li> <li>○ Applications</li> </ul> </li> <li>• RLC resonant circuit design activity</li> </ul> <p><b>Removed Topics</b></p> <ul style="list-style-type: none"> <li>• Tunnel diode</li> <li>• PIN diode</li> <li>• Varactor diode</li> <li>• Schottky diode</li> <li>• Laser diode</li> <li>• CCD</li> <li>• Photovoltaic, photoconductive cells (detailed theory)</li> </ul> <p><b>New Experiments Introduced:</b></p> <ul style="list-style-type: none"> <li>• Transient response of RL and RC circuits</li> <li>• Determination of resonant frequency of series and parallel RLC circuits</li> <li>• Design and analysis of clipper and clamper circuits</li> <li>• Study of Rectifier circuits (half-wave and full-wave)</li> </ul> <p><b>Experiments Removed in R-2025</b></p> <ul style="list-style-type: none"> <li>• CB configuration characteristics</li> <li>• DIAC and TRIAC characteristics</li> <li>• Photodiode and Phototransistor characteristics</li> <li>• Frequency response plotting of tuned circuits</li> </ul>
	225EPP07	Workshop	Newly Introduced Course

**Regulations-2025 PG**

225COT03	<b>Embedded Systems for Communication with AI</b>	<b>NEWLY INTRODUCED COURSE with course modules</b> UNIT I- EMBEDDED SYSTEM ARCHITECTURE & COMMUNICATION FUNDAMENTALS UNIT II -EMBEDDED COMMUNICATION PROTOCOLS & RTOS FOR COMMUNICATION UNIT III-AI/ML FOR EMBEDDED COMMUNICATION SYSTEMS  UNIT IV-EDGE AI HARDWARE PLATFORMS & DEPLOYMENT FOR COMMUNICATION UNIT V -INTELLIGENT EMBEDDED COMMUNICATION SYSTEM DESIGN & CASE STUDIES
225COT04	<b>AI in Signal and Image Processing</b>	<b>NEWLY INTRODUCED COURSE with course modules</b> UNIT I-FUNDAMENTALS OF SIGNAL AND IMAGE PROCESSING  UNIT II-MACHINE LEARNING FOR SIGNAL AND IMAGE PROCESSING UNIT III-DEEP LEARNING FOR SIGNAL AND IMAGE PROCESSING UNIT IV-AI TECHNIQUES FOR SPEECH, VISION, AND PATTERN RECOGNITION UNIT V-APPLICATIONS AND CASE STUDIES
225COP07	<b>Wireless Communication Systems Laboratory</b>	<b>NEWLY INTRODUCED COURSE with course modules</b> <b>LIST OF EXPERIMENTS</b> 1. Spectral Characterisation of Communication Signals (Using Spectrum Analyzer) 2. Design and Analysis of Spectrum Estimators (Bartlett and Welch Methods) 3. Design and Analysis of Digital Modulation Techniques (BPSK, QPSK, QAM, FSK etc.) 4. Simulation of Rayleigh and Rician Fading Channels 5. CDMA Signal Generation and RAKE Receiver Design 6. Design and Performance Analysis of Error Control Encoder and Decoder (Block Codes, Convolutional Codes – Viterbi) 7. Wireless Channel Equalizer Design (ZF / LMS / RLS) 8. Wireless Channel Estimation and Diversity Combining 9. Generation and Analysis of OFDM Signal and PAPR 10.Simulation of MIMO System with Spatial Multiplexing
225COP08	<b>Embedded System Design Laboratory</b>	<b>NEWLY INTRODUCED COURSE with course modules</b> <b>LIST OF EXPERIMENTS</b> <b>1. Programming with 8-bit Microcontrollers – Assembly Programming</b> <ul style="list-style-type: none"> <li>Basic assembly instructions, arithmetic &amp; logical operations</li> <li>Delay generation programs</li> <li>Port manipulation using assembly</li> <li>Memory access and branching instructions</li> </ul> <b>2. Programming with 8-bit Microcontrollers – C Programming</b> <ul style="list-style-type: none"> <li>I/O port programming in C</li> <li>Timer and counter programming</li> <li>Interrupt handling in C</li> <li>Embedded C program structure for microcontrollers</li> </ul> <b>3. Programming with PIC Microcontrollers – Assembly and C</b> <ul style="list-style-type: none"> <li>Programming GPIO, timers, and interrupts</li> </ul> <b>4. I/O Interfacing with PIC Microcontrollers</b>



		<ul style="list-style-type: none"> <li>• PWM generation and motor speed control</li> <li>• ADC and DAC interfacing</li> <li>• LCD modules and sensor interfacing</li> </ul> <b>5. Programming with Raspberry Pi</b> <ul style="list-style-type: none"> <li>• Serial communication and I2C/SPI interfacing</li> <li>• Sensor interfacing</li> </ul> <b>6. Programming with Arduino and Raspberry Pi - I/O Interfacing</b> <ul style="list-style-type: none"> <li>• Timer programming</li> <li>• Interrupt programming</li> <li>• Serial communication</li> <li>• Sensor interfacing (DHT, PIR, accelerometer, gas sensors, etc.)</li> </ul> <p>Small integrated project using Arduino / Raspberry Pi</p>
225COE11	<b>Analog and Mixed Signal VLSI Design</b>	<b>NEWLY INTRODUCED COURSE with course modules</b> UNIT I-INTRODUCTION AND BASIC MOS DEVICES UNIT II -SUBMICRON CIRCUIT DESIGN UNIT III-DATA CONVERTERS UNIT IV-SNR IN DATA CONVERTERS UNIT V -SWITCHED CAPACITOR CIRCUITS
225COE13	<b>Radar Signal Processing</b>	<b>NEWLY INTRODUCED COURSE with course modules</b> UNIT I-INTRODUCTION TO RADAR SYSTEMS UNIT II -SIGNAL MODELS UNIT III-SAMPLING AND QUANTIZATION OF PULSED RADAR SIGNALS UNIT IV-RADAR WAVEFORMS UNIT V -DOPPLER PROCESSING
225COE14	<b>Telecommunication System Modeling and Simulation</b>	<b>NEWLY INTRODUCED COURSE with course modules</b> UNIT I -SIMULATION METHODOLOGY UNIT II-RANDOM SIGNAL GENERATION & PROCESSING UNIT III-MONTE CARLO SIMULATION UNIT IV-ADVANCED MODELS & SIMULATION TECHNIQUES UNIT V -EFFICIENT SIMULATION TECHNIQUES
225COE15	<b>Industrial Internet of Things and Industry 4.0</b>	<b>NEWLY INTRODUCED COURSE with course modules</b> UNIT I-INTRODUCTION TO IIOT AND INDUSTRY 4.0 UNIT II -IIOT ARCHITECTURE, TECHNOLOGIES & COMMUNICATION UNIT III-DATA MANAGEMENT, ANALYTICS & SECURITY IN IIOT UNIT IV-INDUSTRY 4.0 ENABLING TECHNOLOGIES UNIT V -IIOT APPLICATIONS, CASE STUDIES
<ul style="list-style-type: none"> <li>• Feedback obtained about Department Vision, Mission, PEOs, POs and PSOs from stakeholders (Students, Faculty, Employers &amp; Alumni) were also discussed and Stakeholders were satisfied with Vision, Mission PEOs, POs and PSOs.</li> <li>• The department VISION, MISSION, PEOs, POs and PSOs are disseminated in the meeting for the approval based on the feedback obtained from stakeholders.</li> <li>• Feedback obtained about curriculum and syllabus from stakeholders (Students, Faculty, Employers &amp; Alumni) were also discussed and concluded.</li> <li>• The NPTEL (SWAYAM) Courses that are equivalent to the courses in the approved curriculum under the Regulations 2022 (CBCS) registered by the students in the academic year 2024-2025 were considered and forwarded to Academic Council.</li> </ul>		

  
 20/12/25  
 BoS Chairperson




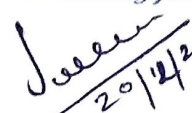
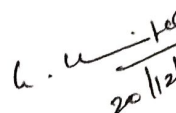



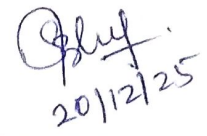

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(AN AUTONOMOUS INSTITUTION), HOSUR

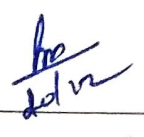
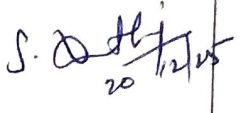
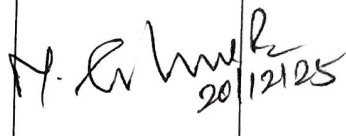


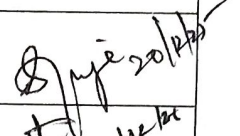
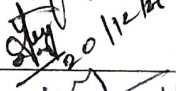
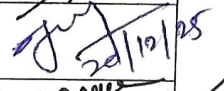
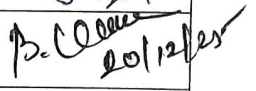
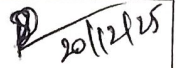
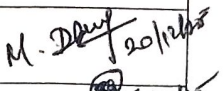
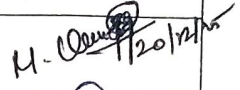
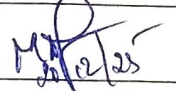
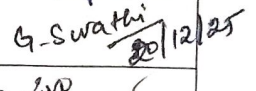
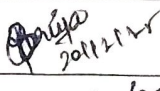
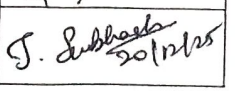
Faculty of Electronics and Communication Engineering

Members present for the **33<sup>rd</sup> Board of Studies** Meeting held on **20.12.2025**

Attendance Sheet

Sl. No	Name and Address of the Member	Category	Signature
1.	Dr.R.Radhakrishnan, M.E., Ph.D., Adhiyamaan College of Engineering, Hosur.	Senior Faculty	
2.	Dr. S.Sumathi, M.E., Ph.D., Prof & Head/Dept. of ECE Adhiyamaan College of Engineering, Hosur.	BoS - Chairperson	 20/12/25
3.	Dr.G.Kavithaa Associate professor, Department of ECE, Government College of Engineering, Salem.	Anna University Nominee	 20/12/25
4.	Dr C Paramasivam, Assistant Professor (SG) Department of ECE, Amrita Vishwa Vidyapeetham, KasavanHalli, Bengaluru	Subject experts from outside the parent university, nominated by Academic Council	 20/12/25
5.	Dr.S.Thenmozhi, Associate professor, Department of ECE, Dayananda Sagar College of Engineering, Bengaluru		 20/12/25
6.	David Kingsley M P Senior Consultant, Capgemini Technologies Pvt Ltd, Kundalahalli, Brookefield, Bengaluru.	Industry Representative	 20-Dec-25
7.	Mrs. Bharani S Lead HR Services, Trellix Cybersecurity, Bangalore		 20/12/25
8.	Ramachandran Krishnaswamy, Senior Technical Leader (Software Engineering), Cisco systems India Pvt Ltd, Bengaluru.	Alumni Representative	 20/12/25



9.	Dr.T Menakadevi, M.E., Ph.D., Professor, Department of ECE Adhiyamaan College of Engineering, Hosur.	Internal Members	
10.	Dr. Xavier Arockiaraj, M.E., Ph.D. Professor, Department of ECE Adhiyamaan College of Engineering, Hosur.		
11.	Dr.M.Ashok Kumar, M.E, Ph.D. Assistant Professor, Department of ECE Adhiyamaan College of Engineering, Hosur		
12.	Mr.P.Manivannan, Assistant Professor/ Department of ECE Adhiyamaan College of Engineering, Hosur.		
13.	Mr.M.Venkatesan, Assistant Professor/ Department of ECE Adhiyamaan College of Engineering, Hosur.		-OD-
14.	Dr.K.Sridharan, Adjunct Faculty, Department of ECE, Adhiyamaan College of Engineering, Hosur.	Adjunct Faculty	
15.	Mrs.V.Sundara Jeyalakshmi, Assistant Professor	Faculty members of ECE	
16.	Mrs.R.Anjana Devi, Assistant Professor		
17.	Mr. C. Stalin Jose, Assistant Professor		
18.	Mrs. B.Uma, Assistant Professor		
19.	Mr.E. Sakthivel, Assistant Professor		
20.	Mr. M. Dhinesh Kumar, Assistant Professor		
21.	Mrs. M. Uma Maheswari, Assistant Professor		
22.	Mr. M. Anbarasan, Assistant Professor		
23.	Mrs. G. Swathi, Assistant Professor		
24.	Mrs. B. Suriya, Assistant Professor		
25.	Mr.J.Subhash, Assistant Professor		

26.	Mrs. Navineshwari.M, Assistant Professor	Faculty members of ECE	<i>[Signature]</i> 20/12/25
27.	Mr. Thiruvarasan.A, Assistant Professor		<i>[Signature]</i> 20/12/25
28.	Mr. Muthu Murugan.B, Assistant Professor		<i>[Signature]</i> 22/12/25
29.	Mrs. Buvanarajani M, Assistant Professor		<i>[Signature]</i> 20/12/25
30.	Ms. Madhushree A, Assistant Professor		<i>[Signature]</i> 20/12/25
31.	Ms. Sabareeshwari Parimalam, Assistant Professor		<i>[Signature]</i> 20/12/25
32.	Mrs. Swetha Bai K, Assistant Professor		<i>[Signature]</i> 20/12/25
33.	Mrs. Thenmozhi M.J, Assistant Professor		<i>[Signature]</i> 22/12/25
34.	Ms. Aishvarya R, Assistant Professor		<i>[Signature]</i> 20/12/25
35.	Mr. Saran Raj S, Assistant Professor		<i>[Signature]</i> 20/12/25
36.	Mr. Selvaraj.K, Assistant Professor		<i>[Signature]</i> 20/12/25
37.	Mrs. Chitravalli.V, Assistant Professor		<i>[Signature]</i> 20/12/25
38.	Mr. Kirubakaran N, Assistant Professor		<i>[Signature]</i> 20/12/25
39.	Mrs. Sulthana Begum J, Assistant Professor		<i>[Signature]</i> 20/12/25
40.	Mrs. Saranya P, Assistant Professor		<i>[Signature]</i> 20/12/25
41.	Mrs. Jayashree R, Assistant Professor		<i>[Signature]</i> 20/12/25
42.	Mrs. Saranya A, Assistant Professor		<i>[Signature]</i> 22/12/25

Date: 20.12.2025

BoS Chairperson

Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UG & P.C)  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.